















ESD

TVS

MOS

LDO

Diode

Sensor

DC-DC

Product Specification

Domestic Part Number	IRLR7843
▶ Overseas Part Number	IRLR7843
▶ Equivalent Part Number	IRLR7843





- ★ Super Low Gate Charge
- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

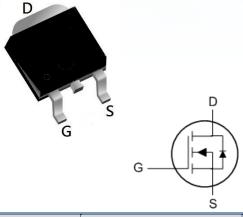
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BVDSS	RDSON ID	
30V	4mΩ	96A

Description

The IRLR7843 is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The IRLR7843 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

TO252 Pin Configuration



Absolute Maximum Ratings

		Ra	Rating		
Symbol	Parameter	10s	Steady State	Units	
V_{DS}	Drain-Source Voltage		30	V	
V_{GS}	Gate-Source Voltage	<u>±</u>	20	V	
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	(96	Α	
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	(68	Α	
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	30	19	Α	
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	25	16	А	
I _{DM}	Pulsed Drain Current ²		192		
EAS	Single Pulse Avalanche Energy ³	gle Pulse Avalanche Energy ³ 144.7		mJ	
I _{AS}	Avalanche Current 53.8		Α		
P _D @T _C =25°C	Total Power Dissipation ⁴	6	62.5		
P _D @T _A =25°C	Total Power Dissipation ⁴	6	2.42	W	
T _{STG}	Storage Temperature Range	-55 t	-55 to 175		
TJ	Operating Junction Temperature Range	-55 t	-55 to 175		

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient ¹		62	°C/W
$R_{ heta JA}$	Thermal Resistance Junction-Ambient ¹ (t ≤10s)		25	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹		2.4	°C/W

Electrical Characteristics ($T_J=25\,$ °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I _D =1mA		0.0213		V/°C
В	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =30A		3.4	4	~~ C
R _{DS(ON)}	Static Dialii-Source Off-Resistance	V _{GS} =4.5V , I _D =15A		5.2	6	mΩ
V _{GS(th)}	Gate Threshold Voltage	V V I 250A	1.0	1.5	2.5	٧
$\triangle V_{GS(th)}$	V _{GS(th)} Temperature Coefficient	$V_{GS}=V_{DS}$, $I_D=250uA$		-5.73		mV/°C
	Dunin Course Lealer as Current	V _{DS} =24V , V _{GS} =0V , T _J =25°C			1	
I _{DSS}	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =55°C			5	- uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =30A		26.5		S
R _g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.4	2.8	Ω
Qg	Total Gate Charge (4.5V)	V _{DS} =15V , V _{GS} =4.5V , I _D =15A		31.6		
Q_gs	Gate-Source Charge			8.6		nC
Q_{gd}	Gate-Drain Charge			11.7		
T _{d(on)}	Turn-On Delay Time			9		
Tr	Rise Time	V_{DD} =15V , V_{GS} =10V , R_{G} =3.3 Ω		19		
T _{d(off)}	Turn-Off Delay Time	I _D =15A		58		ns
T _f	Fall Time			15.2		
C _{iss}	Input Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz		3075	4000	
Coss	Output Capacitance			400	530	рF
C _{rss}	Reverse Transfer Capacitance			315		

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
Is	Continuous Source Current ^{1,5}	V V OV Force Current			96	Α
I _{SM}	Pulsed Source Current ^{2,5}	V _G =V _D =0V , Force Current			192	Α
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C			1	V
t _{rr}	Reverse Recovery Time	IF=30A , dI/dt=100A/μs ,		18		nS
Q _{rr}	Reverse Recovery Charge	T _J =25°C		8		nC

Note:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V, L=0.1mH, I_{AS} =53.8A
- 4.The power dissipation is limited by 175°C junction temperature
- 5. The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

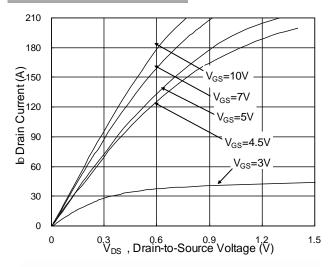


Fig.1 Typical Output Characteristics

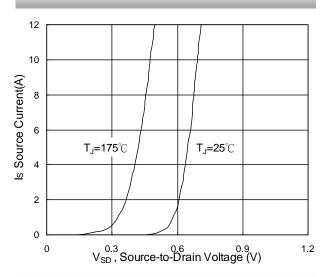


Fig.3 Forward Characteristics of Reverse

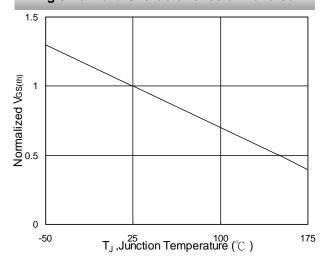


Fig.5 Normalized V_{GS(th)} vs. T_J

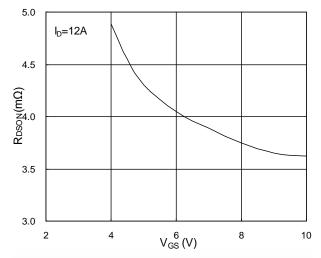


Fig.2 On-Resistance vs. G-S Voltage

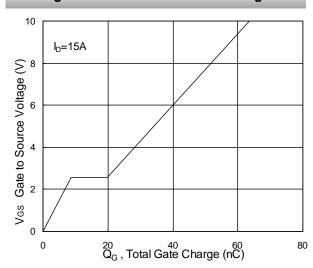


Fig.4 Gate-Charge Characteristics

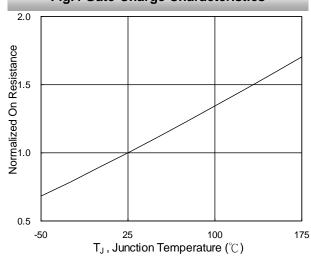
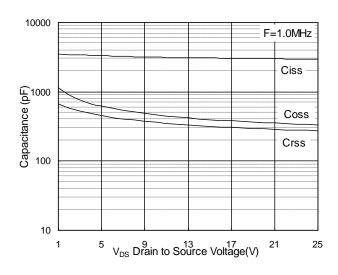


Fig.6 Normalized R_{DSON} vs. T_J



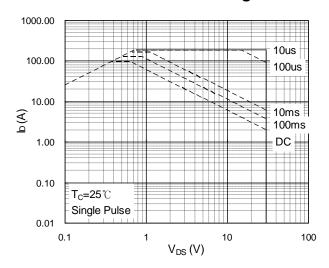


Fig.7 Capacitance

Fig.8 Safe Operating Area

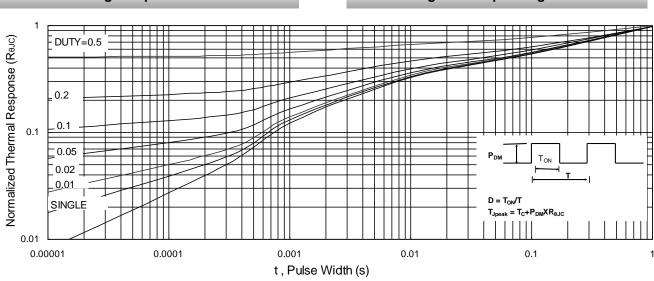


Fig.9 Normalized Maximum Transient Thermal Impedance

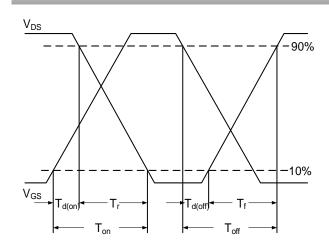


Fig.10 Switching Time Waveform

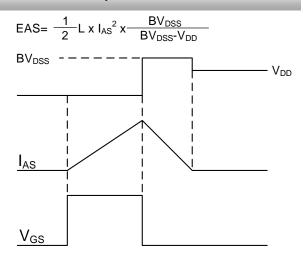


Fig.11 Unclamped Inductive Switching Waveform



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