

EVVOSEMI[®]

THINK CHANGE DO



ESD



TVS



MOS



LDO



Diode



Sensor



DC-DC

Product Specification

▶ Domestic	Part Number	IRFS23N20D
▶ Overseas	Part Number	IRFS23N20D
▶ Equivalent	Part Number	IRFS23N20D

EV is the abbreviation of name EVVO

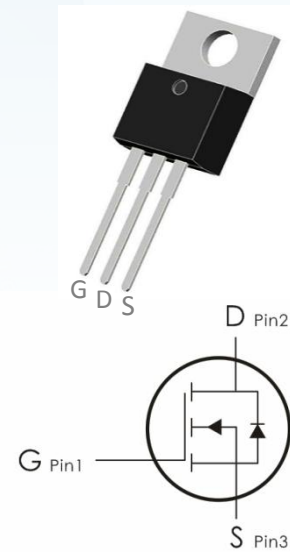
Description:

This N-Channel MOSFET uses advanced Planar technology and design to provide excellent $R_{DS(on)}$ with low gate charge.

It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=200V, I_D=52A, R_{DS(on)} < 60m\ \Omega$ @ $V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(on)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage ¹	200	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	52	A
	Continuous Drain Current- $T_C=100^\circ C$	28	
I_{DM}	Pulsed Drain Current ²	70	
P_D	Power Dissipation	125	W
E_{AS}	Single pulse avalanche energy	1200	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.0	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62	$^\circ C/W$

Electrical Characteristics: ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Sourctce Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	200	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =200V	---	---	1	μ A
		V _{GS} =0V, V _{DS} =160V, T _J =125 °C	---	---	100	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	2	---	4	V
R _{DS(ON)}	Drain-Source On Resistance ⁴	V _{GS} =10V, I _D =20A	---	50	60	m Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	2799	3699	pF
C _{oss}	Output Capacitance		---	304	399	
C _{rss}	Reverse Transfer Capacitance		---	109	149	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =100V, I _D =20A, R _G =3.9 Ω , V _{GS} =10V	---	19	---	ns
t _r	Rise Time		---	29	---	ns
t _{d(off)}	Turn-Off Delay Time		---	64	---	ns
t _f	Fall Time		---	24	---	ns
Q _g	Total Gate Charge	V _{DD} =100V, I _D =20A V _{GS} =0 to 10V	---	96	120	nc
Q _{gs}	Gate-Source Charge		---	13	---	nc
Q _{gd}	Gate-Drain “Miller” Charge		---	38	---	nc
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =40A	---	---	1.5	V
I _S	Continuous Drain Curren ⁴	VD=VG=0V	---	---	52	A
I _{SM}	Pulsed Drain Current ⁴		---	---	70	A
T _{rr}	Reverse Recovery Time	I _F =20A, V _{GS} =0V, dI/dt=100A/us	---	280	---	ns
Q _{rr}	Reverse Recovery Charge	I _F =20A, V _{GS} =0V, dI/dt=100A/us	---	420	---	nc

Notes:

1. $T_J = +25^{\circ}\text{C}$ to $+150^{\circ}\text{C}$
2. Repetitive rating; pulse width limited by maximum junction temperature.
3. $ISD = 20A$ di/dt $< 100 A/\mu s$, $V_{DD} < BV_{DSS}$, $T_J = +150^{\circ}\text{C}$.
4. Pulse width $\leq 380\mu s$; duty cycle $\leq 2\%$

Typical Characteristics: ($T_C = 25^{\circ}\text{C}$ unless otherwise noted)

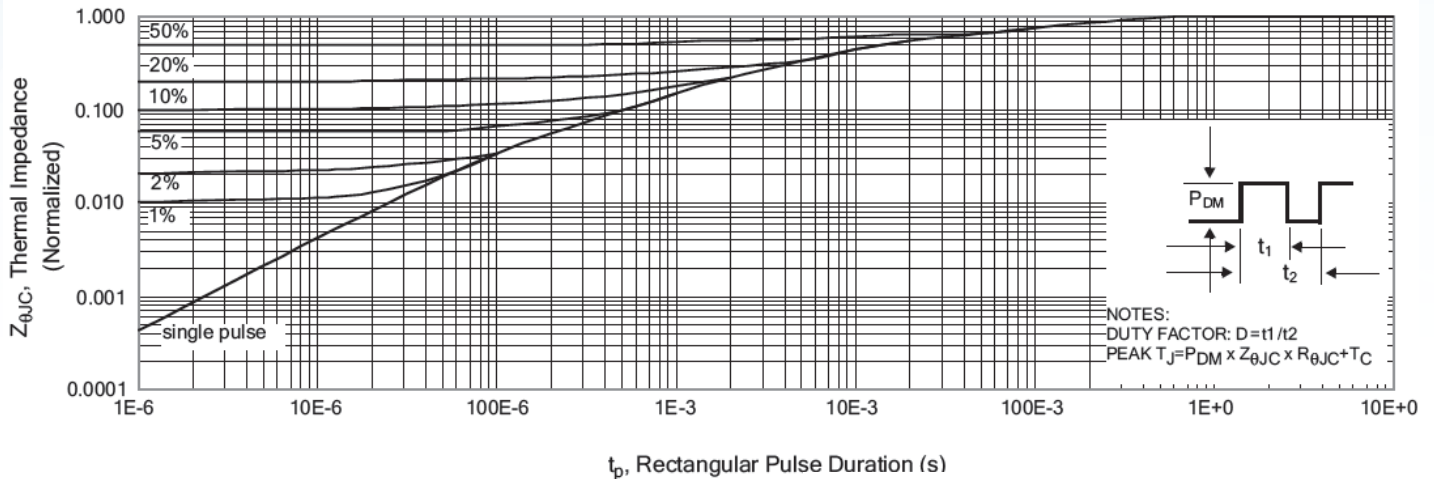


Figure 1. Maximum Effective Thermal Impedance, Junction-to-Case

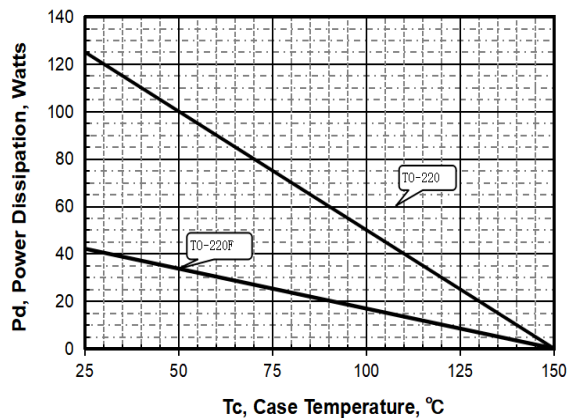


Figure 2. Max. Power Dissipation vs Case Temperature

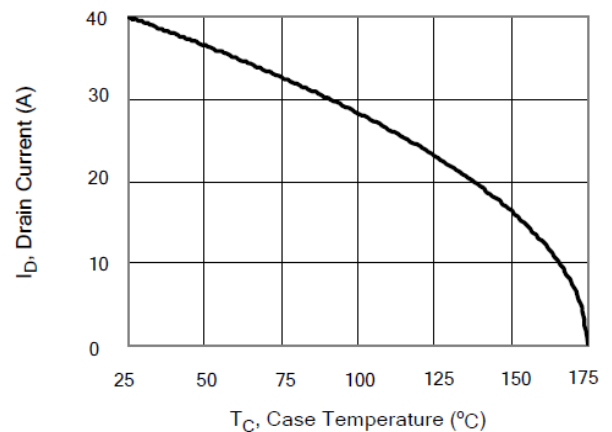


Figure 3. Maximum Continuous Drain Current vs Case Temperature

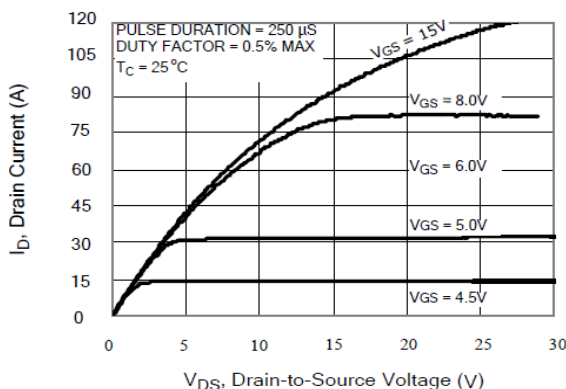


Figure 4. Typical Output Characteristics

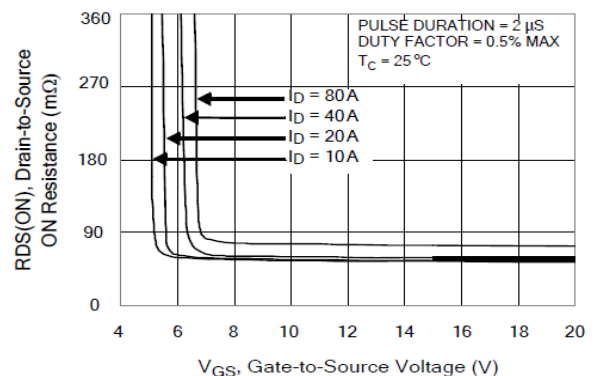
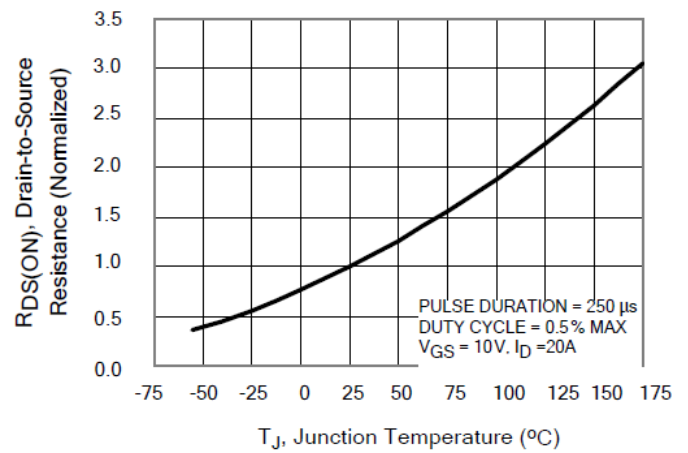
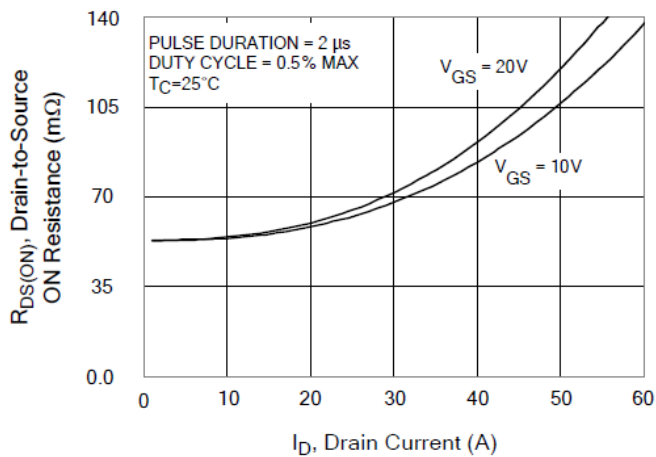
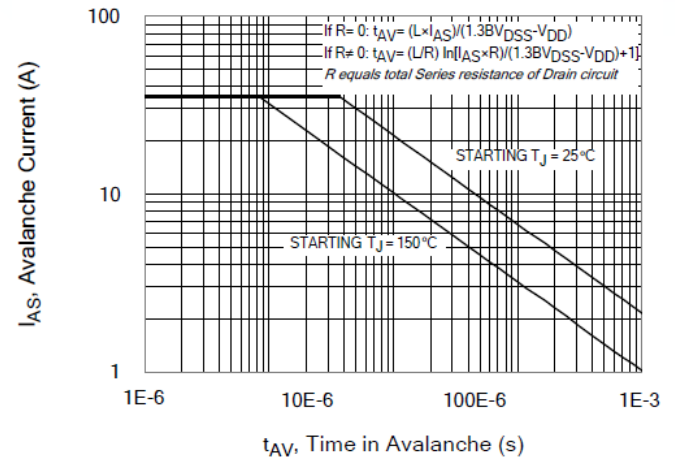
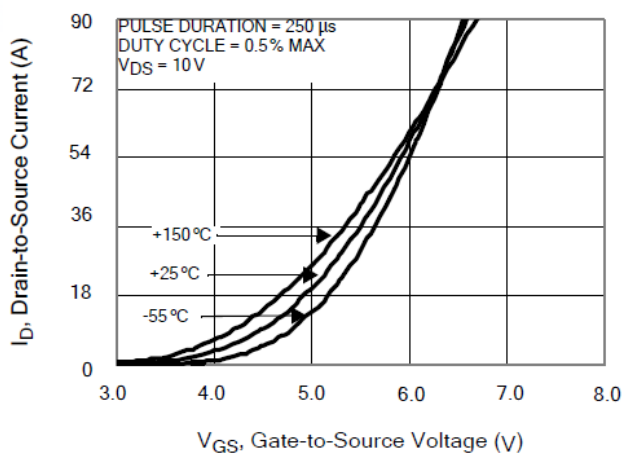
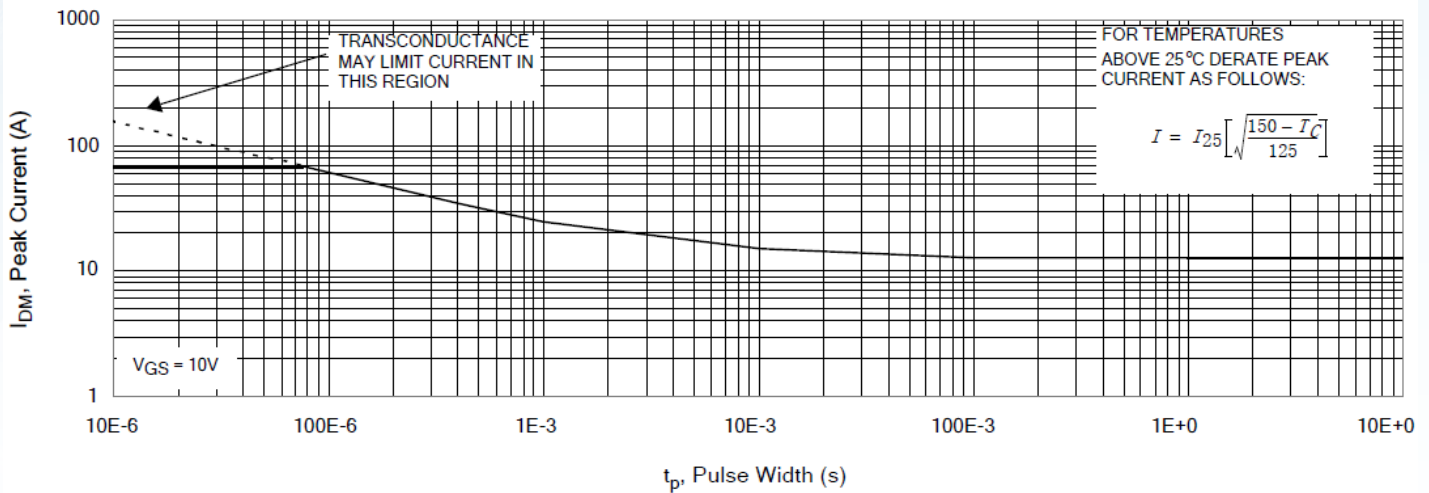


Figure 5. Typical Drain-to-Source ON Resistance vs Gate Voltage and Drain Current



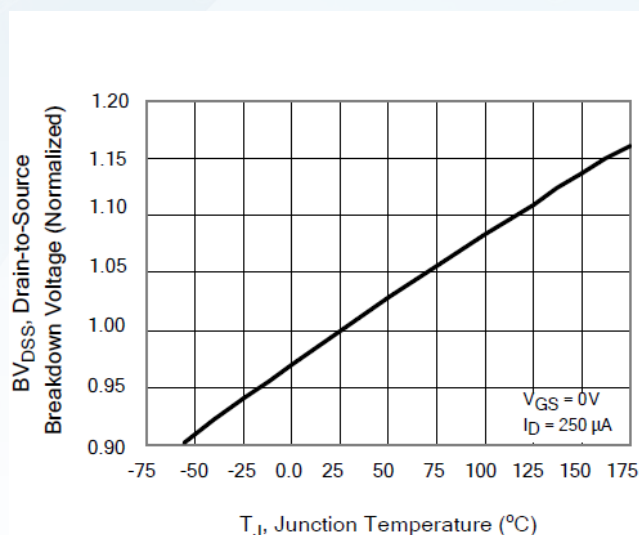


Figure 11. Typical Breakdown Voltage vs Junction Temperature

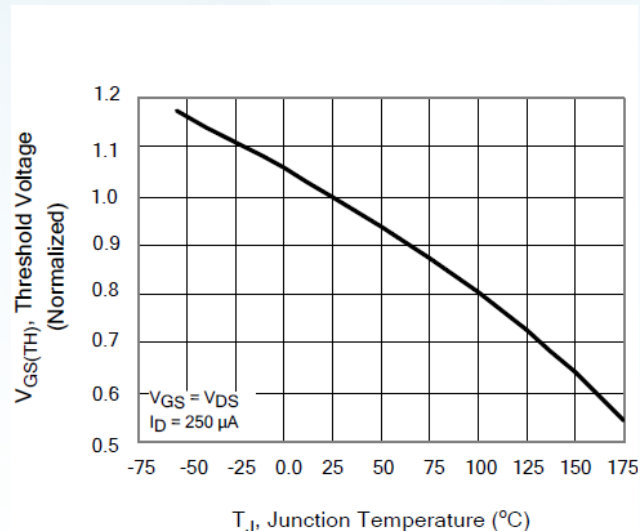


Figure 12. Typical Threshold Voltage vs Junction Temperature

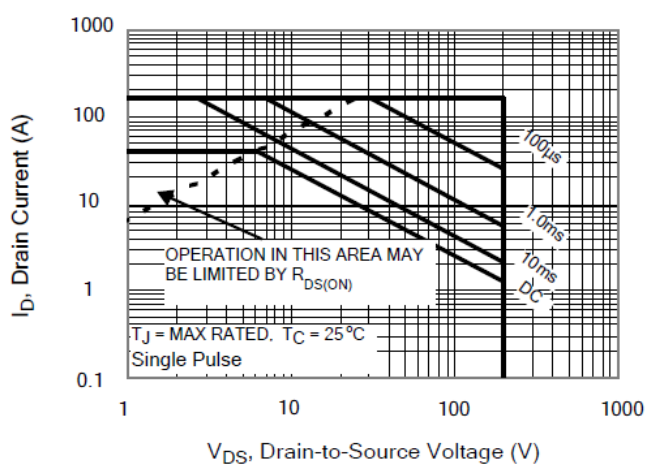


Figure 13. Maximum Forward Bias Safe Operating Area

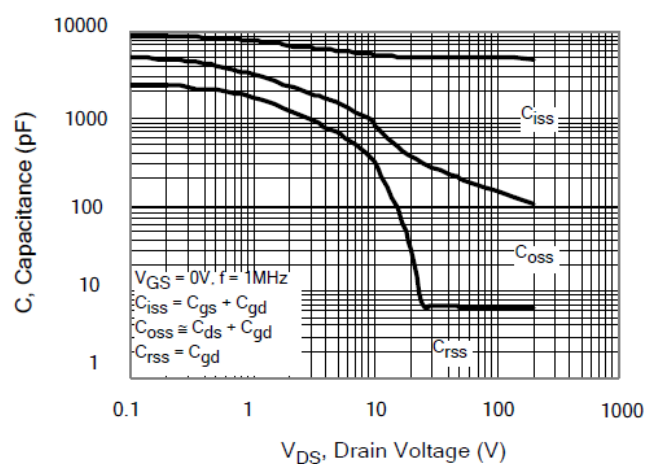


Figure 14. Typical Capacitance vs Drain-to-Source Voltage

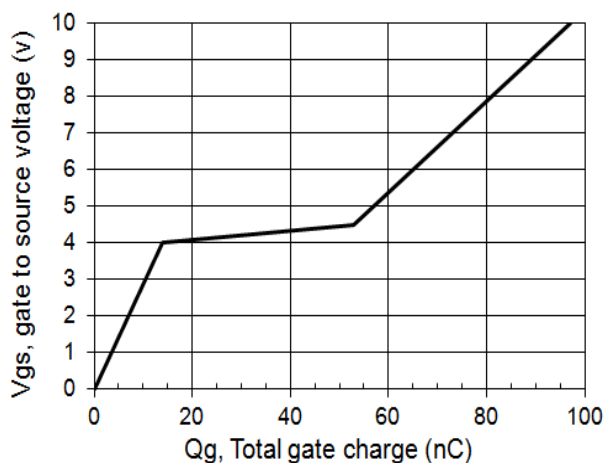


Figure 15. Typical Gate Charge

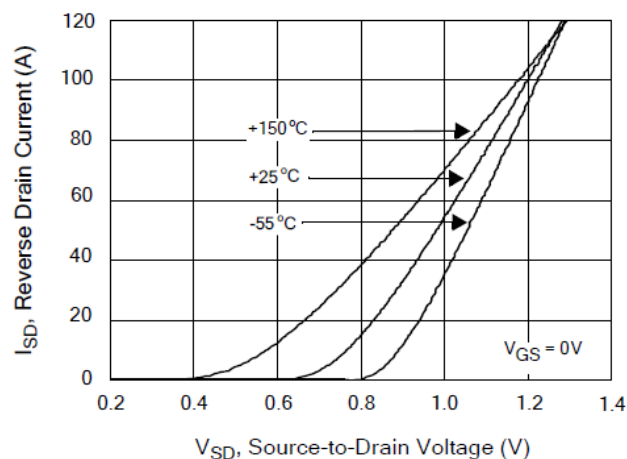
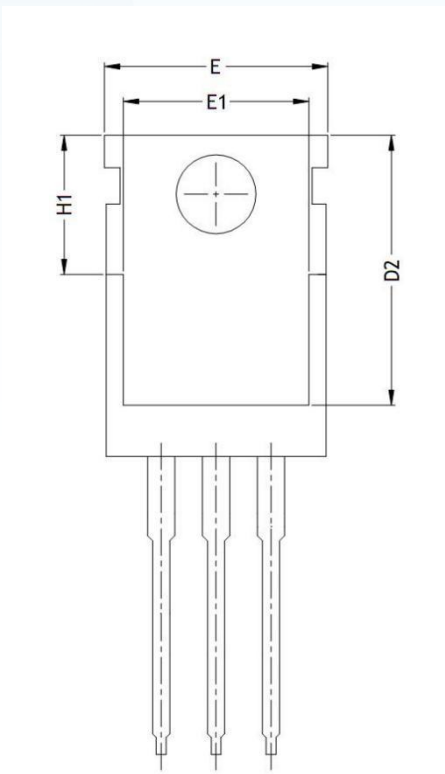
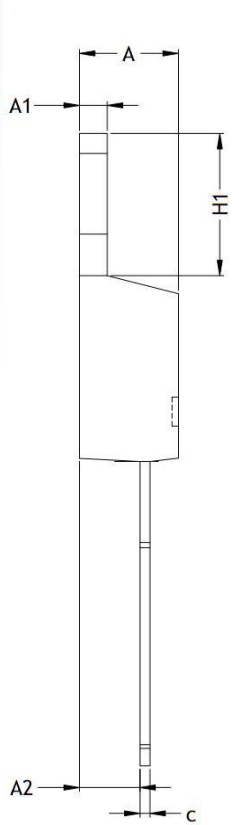
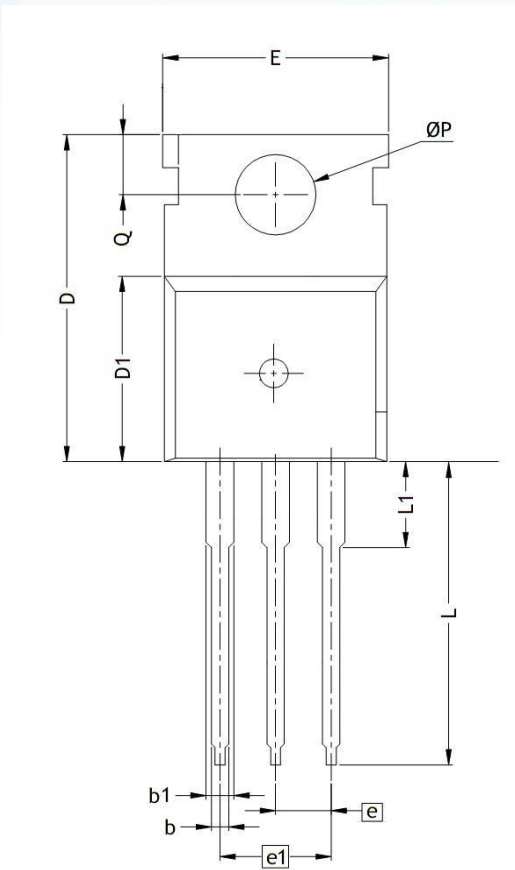


Figure 16. Typical Body Diode Transfer Characteristics

TO-220 Package Information:



UNIT: mm

SYMBOLS	A	A1	A2	b	b1	c	D	D1	D2	E	E1	e
MIN	4.25	1.25	2.35	0.7	1.15	0.45	14.35	8.80	13.05	9.90	7.85	2.540
MAX	4.65	1.35	2.55	0.9	1.75	0.60	15.95	9.50	13.65	10.35	8.85	BSC
SYMBOLS	e1	H1	L	L1	Q	φP						
MIN	5.080	6.30	12.85	2.85	2.70	3.50						
MAX	BSC	6.65	13.50	3.25	2.90	3.70						

Disclaimer

EVVOSEMI ("EVVO") reserves the right to make corrections, enhancements, improvements, and other changes to its products and services at any time, and to discontinue any product or service without notice.

EVVO warrants the performance of its hardware products to the specifications applicable at the time of sale in accordance with its standard warranty. Testing and other quality control techniques are used as deemed necessary by EVVO to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

Customers should obtain and confirm the latest product information and specifications before final design, purchase, or use. EVVO makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does EVVO assume any liability for application assistance or customer product design. EVVO does not warrant or accept any liability for products that are purchased or used for any unintended or unauthorized application.

EVVO products are not authorized for use as critical components in life support devices or systems without the express written approval of EVVOSEMI.

The EVVO logo and EVVOSEMI are trademarks of EVVOSEMI or its subsidiaries in relevant jurisdictions. EVVO reserves the right to make changes without further notice to any products herein.