

EVVOSEMI[®]

THINK CHANGE DO



ESD



TVS



MOS



LDO



Diode



Sensor



DC-DC

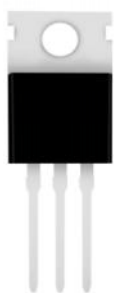
Product Specification

▶ Domestic	Part Number	IRFB38N20D
▶ Overseas	Part Number	IRFB38N20D
▶ Equivalent	Part Number	IRFB38N20D

EV is the abbreviation of name EVVO

Description

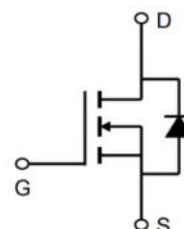
The IRFB38N20D is silicon N-channel Enhanced VDMOSFETs, is obtained by the self-aligned planar Technology which reduce the conduction loss, improve switching performance and enhance the avalanche energy. The transistor can be used in various power switching circuit for system miniaturization and higher efficiency.



TO-220-3L



TO-220F



250V N-Channel Enhancement Mode MOSFET

Product Summary

$V_{DS} = 250V, I_D = 50A$

$R_{DS(ON)} < 85m\Omega @ V_{GS} = 10V$

Application

Power amplifier

Pin Configuration

Absolute Maximum Ratings

Symbol	Parameter	Value	Unit
VDSS	Drain-Source Voltage ($V_{GS} = 0V$)	250	V
ID	Continuous Drain Current	50	A
IDM	Pulsed Drain Current	180	A
VGSS	Gate-Source Voltage	± 30	V
EAS	Single Pulse Avalanche Energy	973	mJ
IAS	Avalanche Current	36	A
EAR	Repetitive Avalanche Energy	584	mJ
PD	Power Dissipation ($T_C = 25^\circ C$)	65	W
TJ, Tstg	Operating Junction and Storage Temperature Range	-55~+150	$^\circ C$
RthJC	Thermal Resistance, Junction-to-Case	0.89	K/W
RthJA	Thermal Resistance, Junction-to-Ambient	60	K/W

250V N-Channel Enhancement Mode MOSFET
Electrical Characteristics (T_J=25°C unless otherwise specified)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
V(BR)DSS	Drain-Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	250	--	--	V
IDSS	Zero Gate Voltage Drain Current	V _{DS} = 250V, V _{GS} = 0V, T _J = 25°C	--	--	1	μA
IGSS	Gate-Source Leakage	V _{GS} = ±30V	--	--	±100	nA
VGS(th)	Gate-Source Threshold Voltage	V _{DS} = V _{GS} , I _D = 250μA	2	--	4	V
RDS(on)	Drain-Source On-Resistance (Note3)	V _{GS} = 10V, I _D = 22.5A	--	70	85	mΩ
Ciss	Input Capacitance	V _{GS} = 0V, V _{DS} = 25V, f = 1.0MHz	--	3539	--	pF
Coss	Output Capacitance		--	535	--	
Crss	Reverse Transfer Capacitance		--	309	--	
Qg	Total Gate Charge	V _{DD} = 200V, I _D = 45A, V _{GS} = 10V	--	244	--	nC
Qgs	Gate-Source Charge		--	16	--	
Qgd	Gate-Drain Charge		--	143	--	
td(on)	Turn-on Delay Time	V _{DD} = 125V, I _D = 45A, R _G = 25 Ω	--	57	--	ns
t _r	Turn-on Rise Time		--	145	--	
td(off)	Turn-off Delay Time		--	960	--	
t _f	Turn-off Fall Time		--	235	--	
IS	Continuous Body Diode Current	T _C = 25 °C	--	--	45	A
ISM	Pulsed Diode Forward Current		--	--	180	
VSD	Body Diode Voltage	T _J = 25°C, I _{SD} = 22.5A, V _{GS} = 0V	--	--	1.4	V
trr	Reverse Recovery Time	V _{GS} = 0V, I _S = 10A, di _F /dt = 100A/μs	--	264	--	ns
Qrr	Reverse Recovery Charge		--	3	--	μC

Notes

- 1、Repetitive Rating: Pulse width limited by maximum junction temperature
- 2、L = 1.0mH, V_{DD} = 50V, R_G = 25 Ω, Starting T_J = 25 °C
- 3、Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%

250V N-Channel Enhancement Mode MOSFET

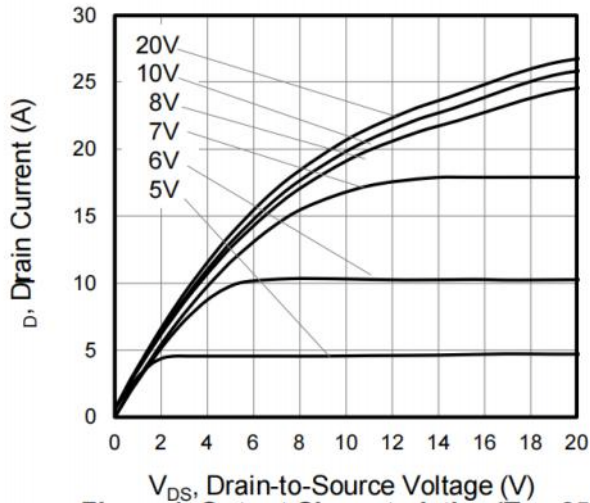


Figure 1. Output Characteristics ($T_J = 25^\circ\text{C}$)

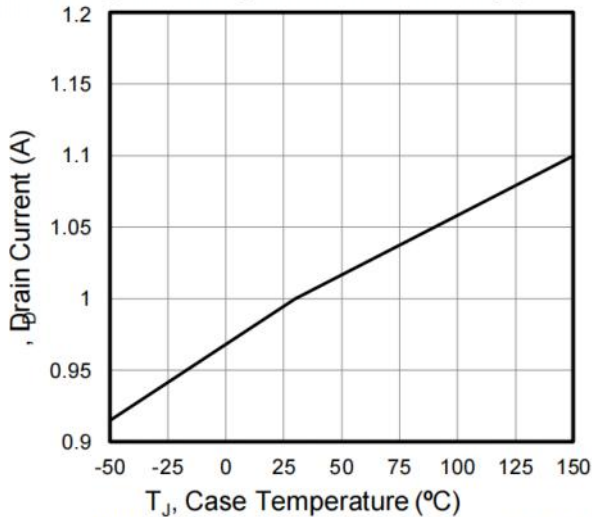


Figure 3. Drain Current vs. Temperature

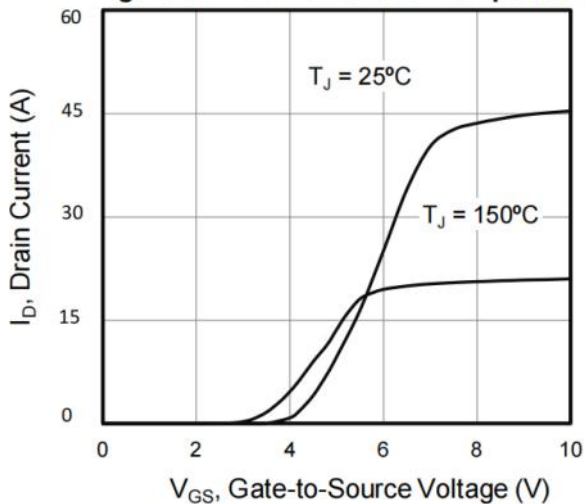


Figure 5. Transfer Characteristics

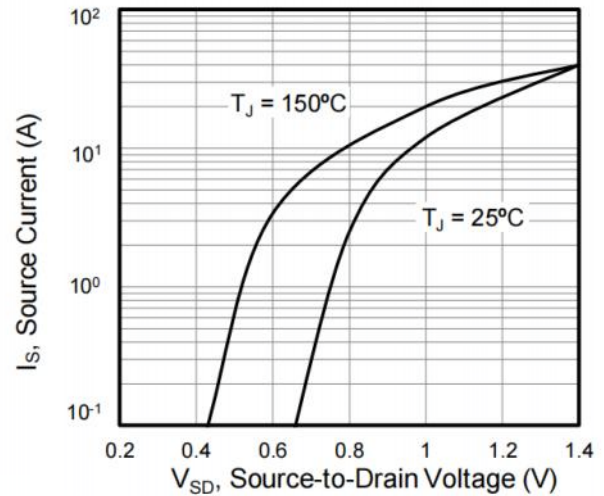


Figure 2. Body Diode Forward Voltage

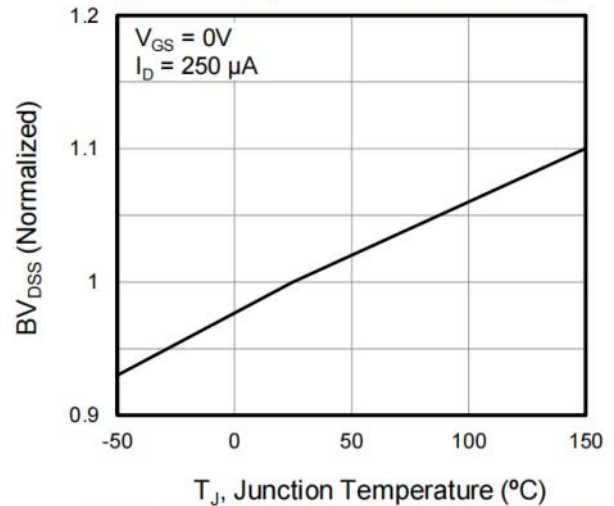


Figure 4. BV_{DSS} Variation vs. Temperature

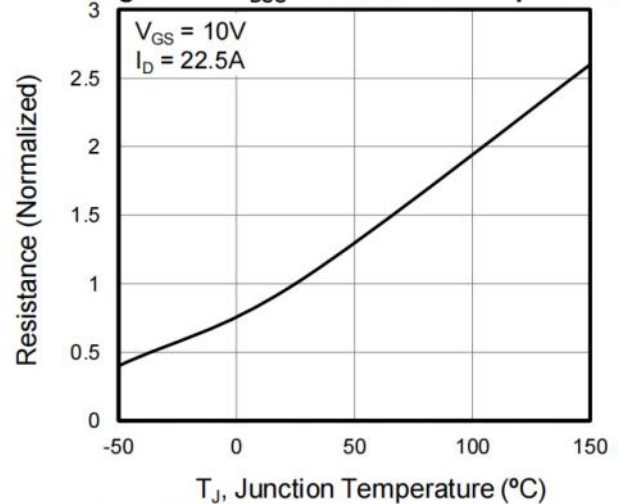
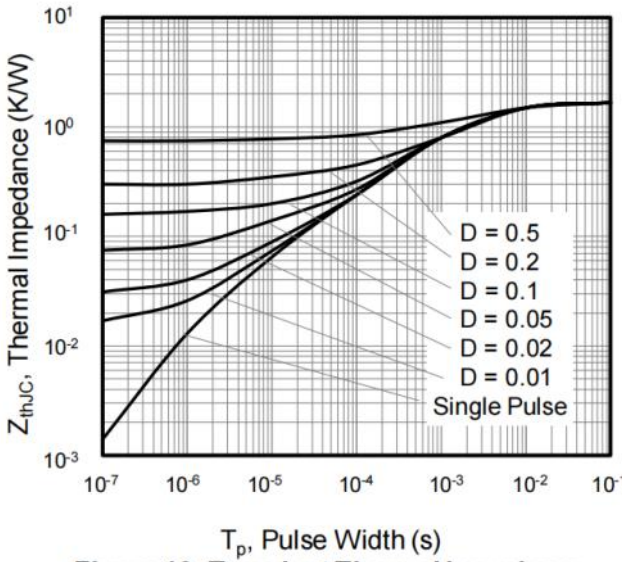
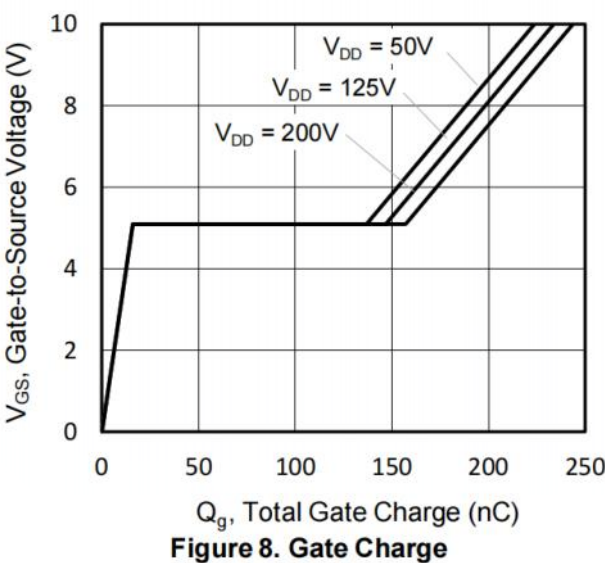
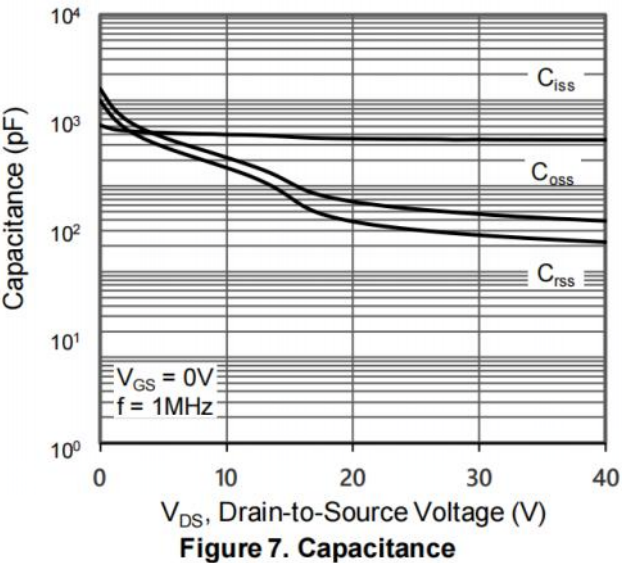


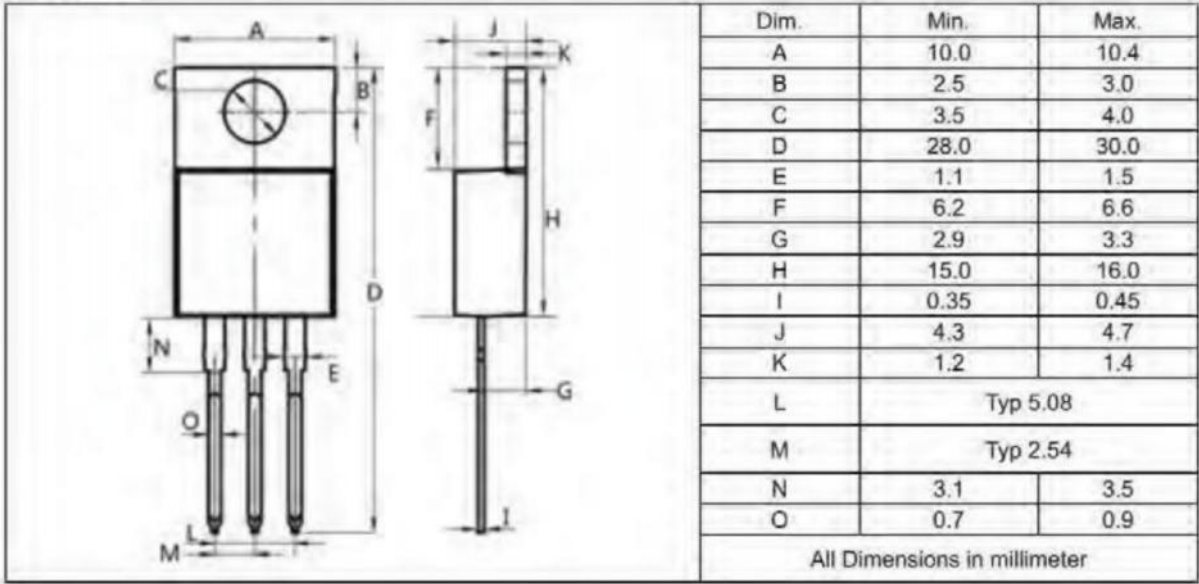
Figure 6. On-Resistance vs. Temperature

250V N-Channel Enhancement Mode MOSFET

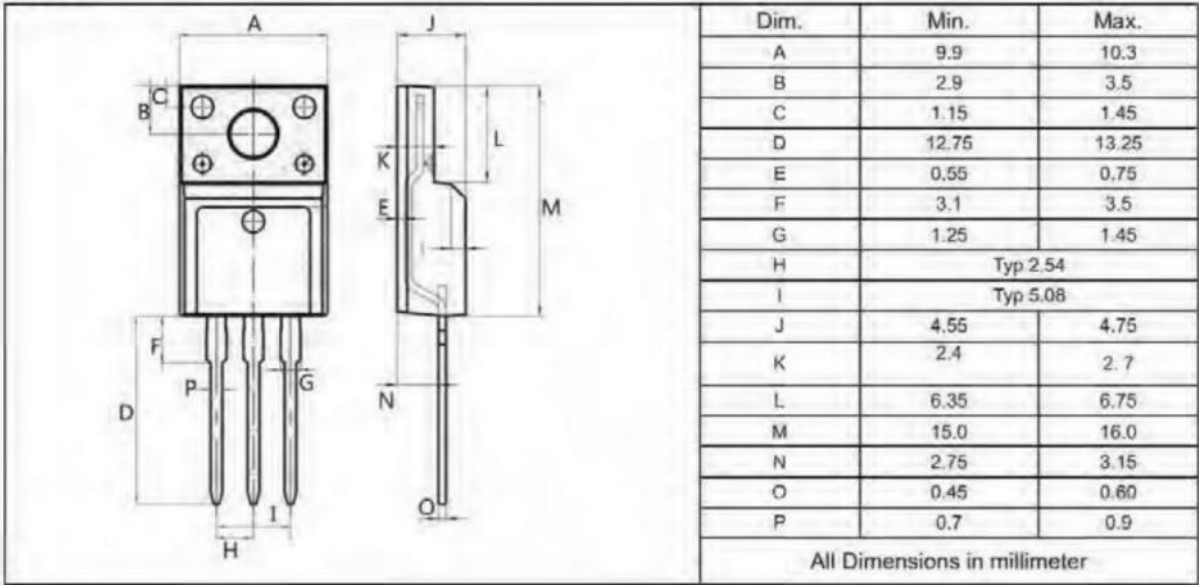


250V N-Channel Enhancement Mode MOSFET

TO-220-3L



TO-220F



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