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ESD



TVS



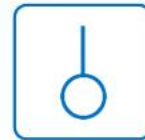
MOS



LDO



Diode



Sensor



DC-DC

Product Specification

▶ Domestic	Part Number	IPD60R360P7
▶ Overseas	Part Number	IPD60R360P7
▶ Equivalent	Part Number	IPD60R360P7

EV is the abbreviation of name EVVO

N-channel 650V, 11A, 0.38Ω Super-Junction Power MOSFET

Description

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFET, designed according to the SJ principle. The resulting device has extremely low on resistance, making it especially suitable for applications which require superior power density and outstanding efficiency.

Features

- ◆ Very low FOM $R_{DS(on)} \times Q_g$
- ◆ 100% UIS tested
- ◆ RoHS compliant

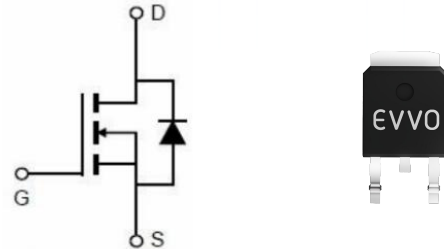
Applications

- ◆ Power factor correction (PFC).
- ◆ Switched mode power supplies (SMPS).
- ◆ Uninterrupted power supply (UPS).

Product Summary

$V_{DS} @ T_{j,25^\circ C}$	650V
$R_{DS(on),max}$	0.38 Ω
I_D	11A
$Q_{g,typ}$	19.2nC

TO-252-2L Pin Configuration



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	650	V
Continuous drain current ($T_C = 25^\circ C$)	I_D	11	A
($T_C = 100^\circ C$)		7	A
Pulsed drain current ¹⁾	I_{DM}	33	A
Gate-Source voltage	V_{GSS}	± 30	V
Avalanche energy, single pulse ²⁾	E_{AS}	210	mJ
Avalanche current, repetitive ³⁾	I_{AR}	1.6	A
Power Dissipation TO-263 ($T_C = 25^\circ C$)	P_D	125	W
- Derate above 25°C		1	W/°C
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C
Continuous diode forward current	I_S	11	A
Diode pulse current	$I_{S,pulse}$	33	A

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Thermal Characteristics

Parameter	Symbol	Value	Unit
		TO-263	
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1	$^{\circ}\text{C/W}$
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	$^{\circ}\text{C/W}$
Soldering temperature, wave soldering only allowed at leads. (1.6mm from case for 10s)	T_{sold}	260	$^{\circ}\text{C}$

Electrical Characteristics

 $T_c = 25^{\circ}\text{C}$ unless otherwise noted

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static characteristics						
Drain-source breakdown voltage	BV _{DSS}	V _{GS} =0 V, I _D =250uA	650	-	-	V
Gate threshold voltage	V _{GE(th)}	V _{GS} =V _{DS} , I _D =250uA	2.0	3.0	4.0	V
Drain cut-off current	I _{DSS}	V _{DS} =650 V, V _{GS} =0 V, T _J = 25°C T _J = 125°C	- -	- 10	1	μA
Gate leakage current, Forward	I _{GSSF}	V _{GS} =30 V, V _{DS} =0 V	-	-	100	nA
Gate leakage current, Reverse	I _{GSSR}	V _{GS} =-30 V, V _{DS} =0 V	-	-	-100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =5.5 A T _J = 25°C	- - -	330	380	mΩ
Dynamic characteristics						
Input capacitance	C _{iss}	V _{DS} = 100 V, V _{GS} = 0 V, f = 1MHz	-	852	-	pF
Output capacitance	C _{oss}		-	37	-	
Reverse transfer capacitance	C _{rss}		-	2.0	-	
Turn-on delay time	t _{d(on)}	V _{DD} = 400V, I _D = 5.5A R _G = 25Ω, V _{GS} =10V	-	16	-	ns
Rise time	t _r		-	35	-	
Turn-off delay time	t _{d(off)}		-	78	-	
Fall time	t _f		-	39.5	-	
Gate charge characteristics						
Gate to source charge	Q _{gs}	V _{DD} =520 V, I _D =5.5A, V _{GS} =0 to 10 V	-	3.1	-	nC
Gate to drain charge	Q _{gd}		-	8.2	-	
Gate charge total	Q _g		-	19.2	-	
Gate plateau voltage	V _{plateau}		-	5.5	-	V
Reverse diode characteristics						
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _S =11A	-	-	1.4	V
Reverse recovery time	t _{rr}	V _R =400 V, I _F =5.5A, dI _F /dt=100 A/μs	-	310	-	ns
Reverse recovery charge	Q _{rr}		-	2.8	-	μC
Peak reverse recovery current	I _{rm}		-	16	-	A

Notes:

- Limited by maximum junction temperature, maximum duty cycle is 0.75.
- $I_{\text{AS}} = 3\text{ A}, V_{\text{DD}} = 50\text{ V},$ Starting $T_{\text{J}} = 25^{\circ}\text{C}.$

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Electrical Characteristics Diagrams

Figure 1. Output Characteristics

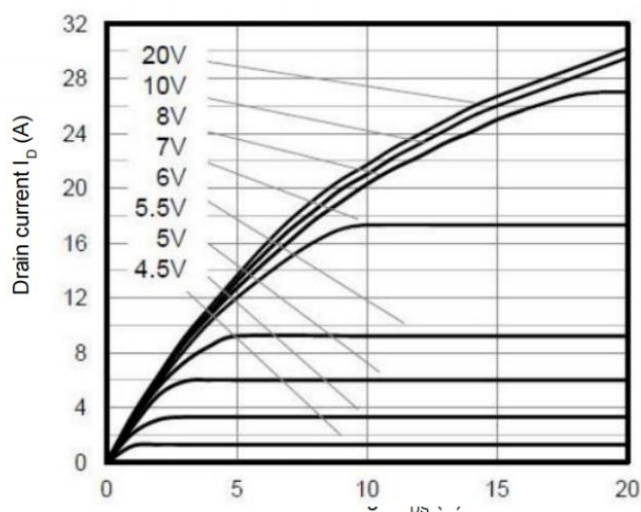


Figure 2. Transfer Characteristics

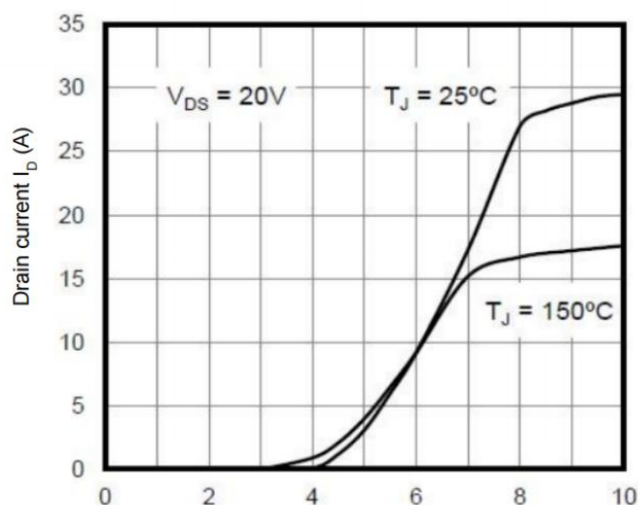


Figure 3. On-Resistance vs. Drain Current

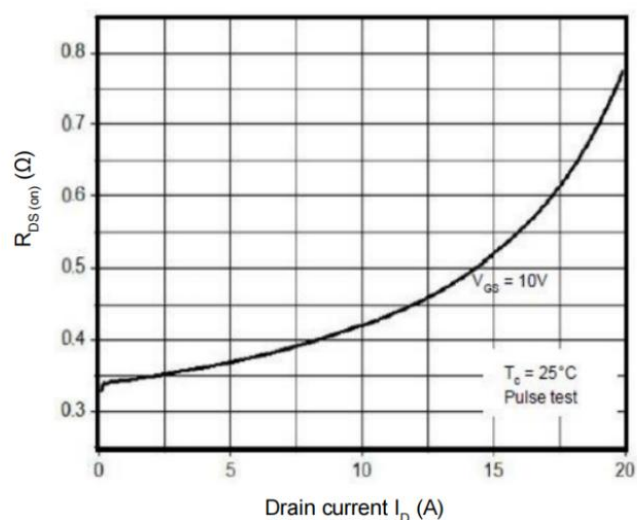


Figure 4. Capacitance Characteristics

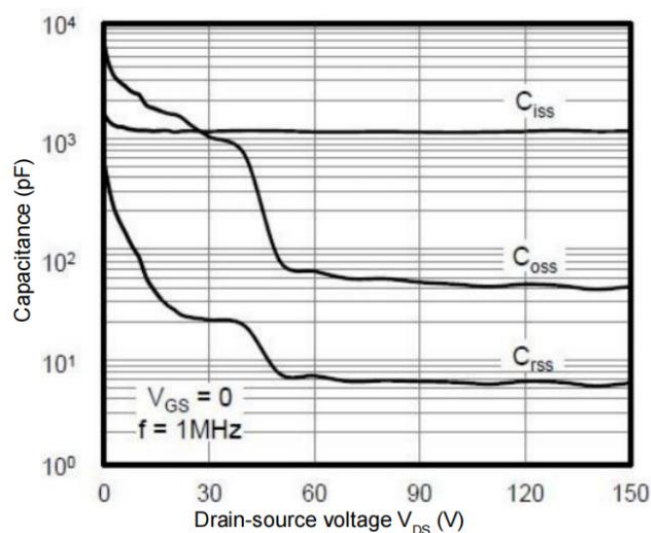


Figure 5. Gate Charge Characteristics

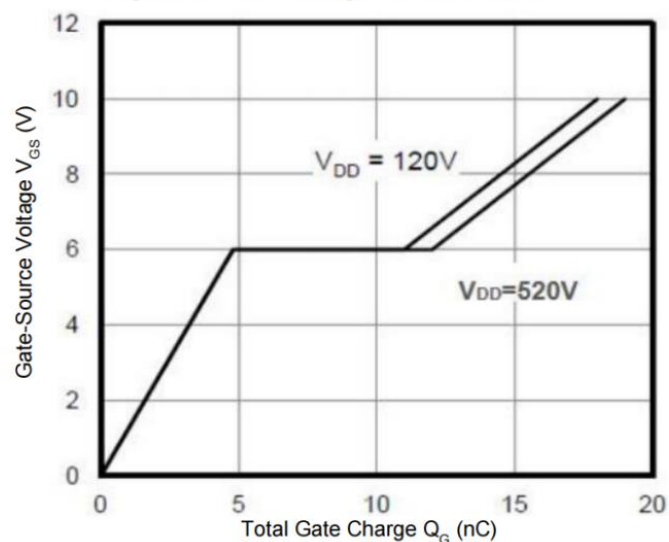
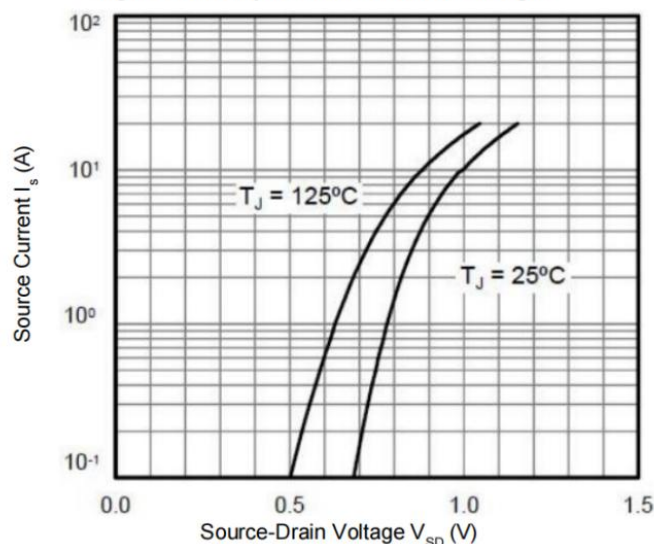


Figure 6. Body Diode Forward Voltage



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Figure 7. Breakdown Voltage vs. Temperature

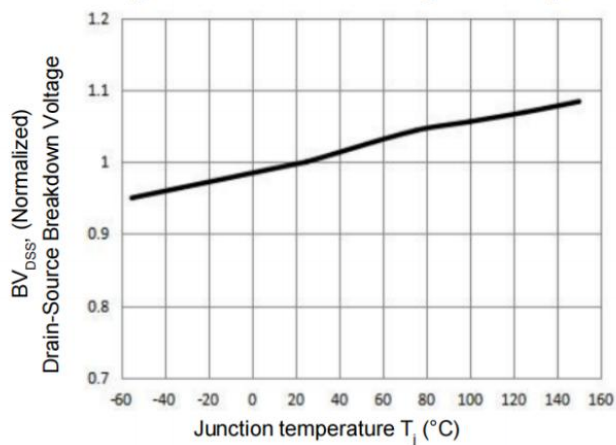


Figure 8. On-Resistance vs. Temperature

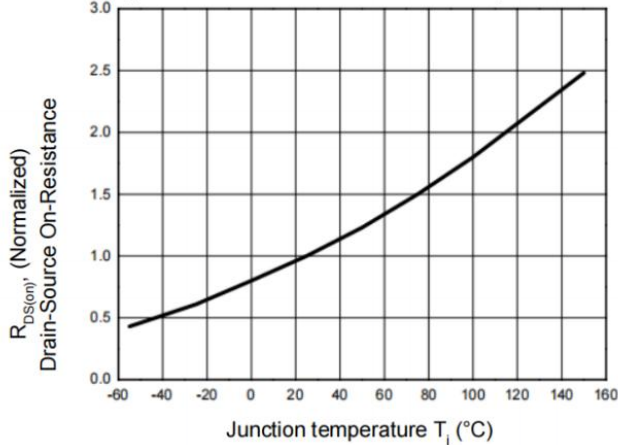
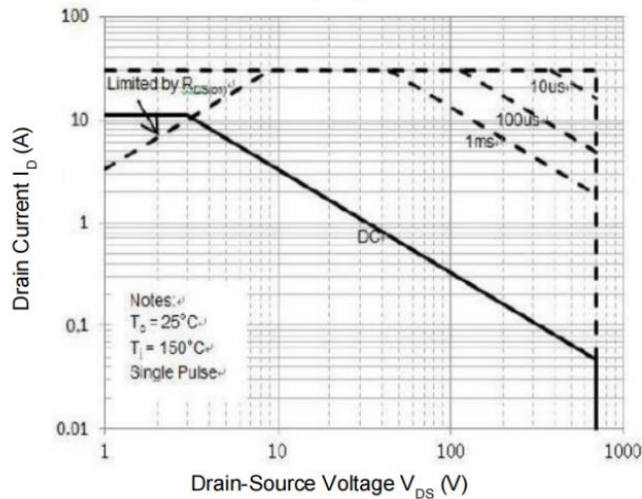


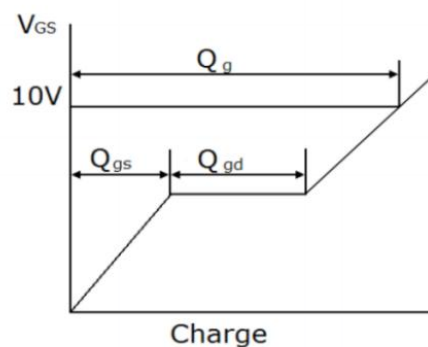
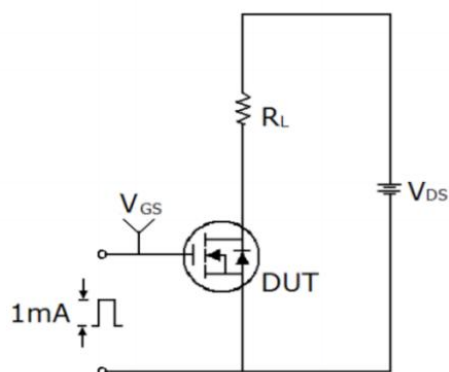
Figure 9. Maximum Safe Operating Area
TO-263



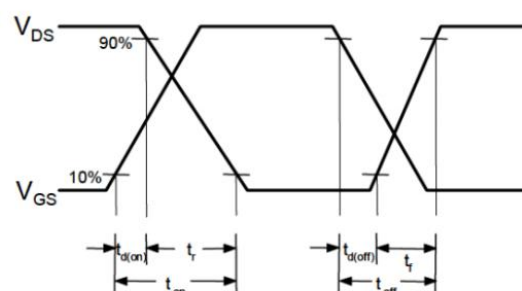
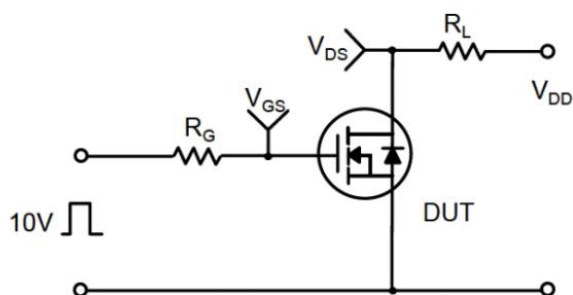
N-channel 650V, 11A, 0.38Ω Super-Junction Power MOSFET

Test Circuits

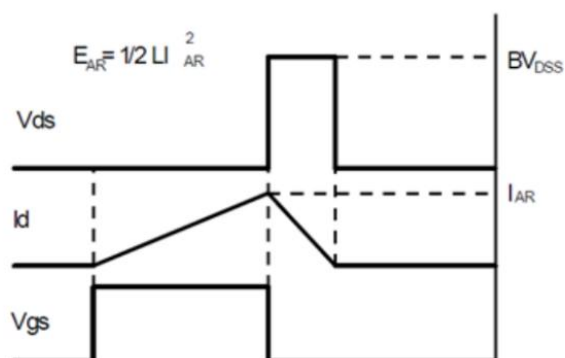
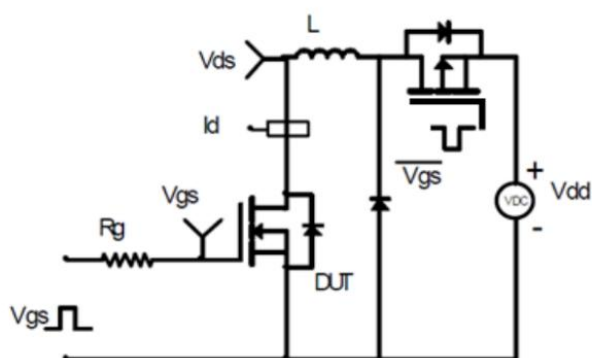
Gate Charge Test Circuit & Waveform



Switching Test Circuit & Waveform

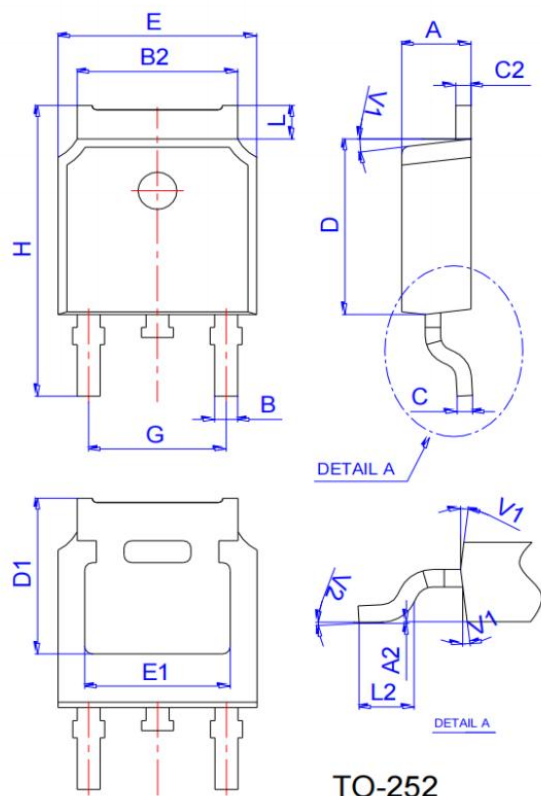


Unclamped Inductive Switching Test Circuit & Waveform



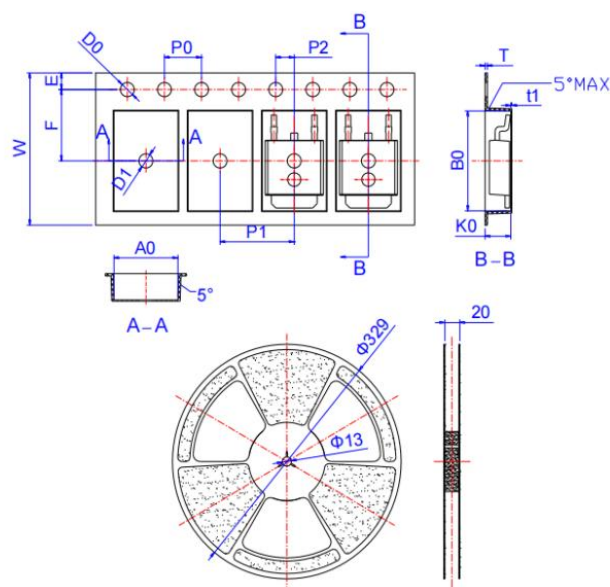
N-channel 650V, 11A, 0.38Ω Super-Junction Power MOSFET

Package Mechanical Data



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Reel Specification-TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
E	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.40	1.50	1.60	0.055	0.059	0.063
D1	1.40	1.50	1.60	0.055	0.059	0.063
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
A0	6.85	6.90	7.00	0.270	0.271	0.276
B0	10.45	10.50	10.60	0.411	0.413	0.417
K0	2.68	2.78	2.88	0.105	0.109	0.113
T	0.24		0.27	0.009		0.011
t1	0.10			0.004		
10P0	39.80	40.00	40.20	1.567	1.575	1.583

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