

# EVVOSEMI<sup>®</sup>

THINK CHANGE DO



ESD



TVS



MOS



LDO



Diode



Sensor



DC-DC

## Product Specification

▶ Domestic	Part Number	IRF7380
▶ Overseas	Part Number	IRF7380
▶ Equivalent	Part Number	IRF7380

EV is the abbreviation of name EVVO

# 100V N+N-Channel Enhancement Mode MOSFET

## Description

The IRF7380 is the high cell density trench N-channel MOSFETs, which provide excellent  $R_{DS(on)}$  and gate charge for most of the synchronous buck converter applications.

The IRF7380 meet the RoHS and Green Product

- ★ Green Device Available
- ★ Super Low Gate Charge
- ★ Excellent  $CdV/dt$  effect decline
- ★ Advanced high cell density Trench technology

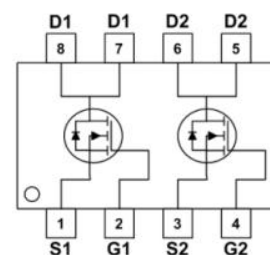
## Product Summary

$V_{DS} = 100V$   $I_D = 10A$

$R_{DS(on)} = 68\text{ m}\Omega$  @  $V_{GS} = 10V$

$R_{DS(on)} = 78\text{ m}\Omega$  @  $V_{GS} = 4.5V$

## SOP-8L Pin Configuration



## Absolute Maximum Ratings ( $T_A = 25^\circ\text{C}$ , unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		$V_{DS}$	100	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$T_A = 25^\circ\text{C}$	$I_D$	10	A
	$T_A = 100^\circ\text{C}$		3.5	
Pulsed Drain Current <sup>1</sup>		$I_{DM}$	16	A
Single Pulse Avalanche Energy <sup>2</sup>		EAS	3.2	mJ
Total Power Dissipation	$T_A = 25^\circ\text{C}$	$P_D$	3.1	W
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

## Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>3</sup>	$R_{\theta JA}$	40.3	$^\circ\text{C/W}$

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## Electrical Characteristics (T<sub>J</sub> = 25°C, unless otherwise noted)

Parameter		Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics							
Drain-Source Breakdown Voltage		V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	100	-	-	V
Gate-Body Leakage Current		I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T <sub>J</sub> =25°C	I <sub>DSS</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> = 0V	-	-	1	μA
	T <sub>J</sub> =100°C			-	-	100	
Gate-Threshold Voltage		V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	1	1.7	3	V
Drain-Source on-Resistance <sup>4</sup>		R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 4A	-	68	100	mΩ
			V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2A	-	78	110	
Forward Transconductance <sup>4</sup>		g <sub>fs</sub>	V <sub>DS</sub> =10V , I <sub>D</sub> =4A	-	11	-	S
Dynamic Characteristics <sup>5</sup>							
Input Capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 50V, V <sub>GS</sub> =0V, f =1MHz	-	1233	-	pF
Output Capacitance		C <sub>oss</sub>		-	32	-	
Reverse Transfer Capacitance		C <sub>rss</sub>		-	26	-	
Gate Resistance		R <sub>g</sub>	f =1MHz	-	1.4	-	Ω
Switching Characteristics <sup>5</sup>							
Total Gate Charge		Q <sub>g</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, I <sub>D</sub> =4A	-	12	-	nC
Gate-Source Charge		Q <sub>gs</sub>		-	2.9	-	
Gate-Drain Charge		Q <sub>gd</sub>		-	1.8	-	
Turn-on Delay Time		t <sub>d(on)</sub>	V <sub>GS</sub> =10V, V <sub>DD</sub> =50V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = 4A	-	3.9	-	ns
Rise Time		t <sub>r</sub>		-	26	-	
Turn-off Delay Time		t <sub>d(off)</sub>		-	16.2	-	
Fall Time		t <sub>f</sub>		-	8.9	-	
Body Diode Reverse Recovery Time		t <sub>rr</sub>	I <sub>F</sub> = 4A, dI/dt=100A/μs	-	40	-	ns
Body Diode Reverse Recovery Charge		Q <sub>rr</sub>		-	43	-	nC
Drain-Source Body Diode Characteristics							
Diode Forward Voltage <sup>4</sup>		V <sub>SD</sub>	I <sub>S</sub> = 1A, V <sub>GS</sub> = 0V	-	-	1.2	V
Continuous Source Current	T <sub>A</sub> =25°C	I <sub>S</sub>	-	-	-	10	A

### Notes:

1. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
2. The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>= 8A .
3. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, The value in any given application depends on the user's specific board design.
4. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
5. This value is guaranteed by design hence it is not included in the production test..

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Typical Characteristics

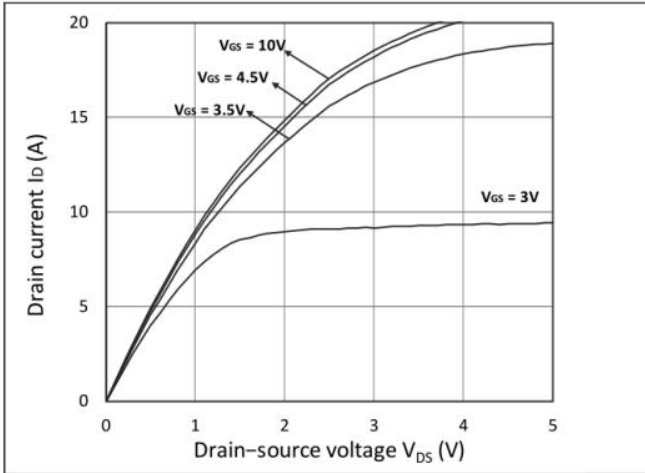


Figure 1. Output Characteristics

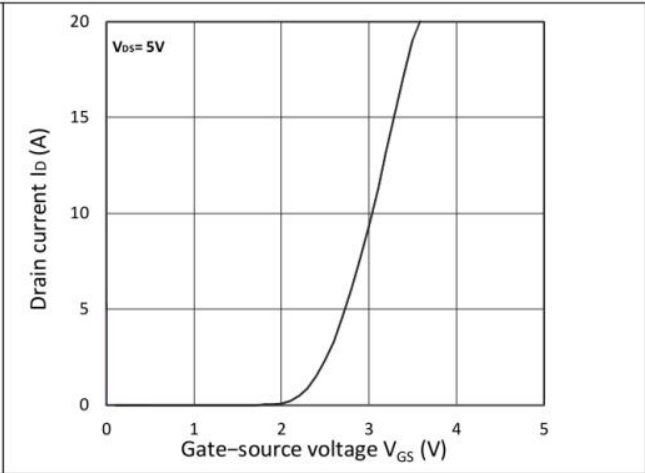


Figure 2. Transfer Characteristics

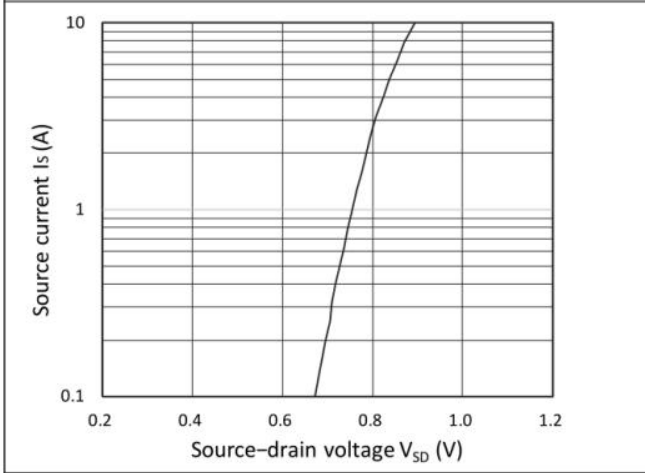


Figure 3. Forward Characteristics of Reverse

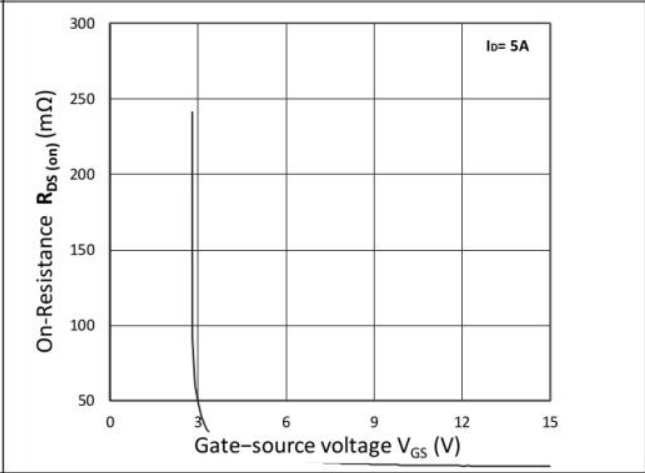


Figure 4.  $R_{DS(on)}$  vs.  $V_{GS}$

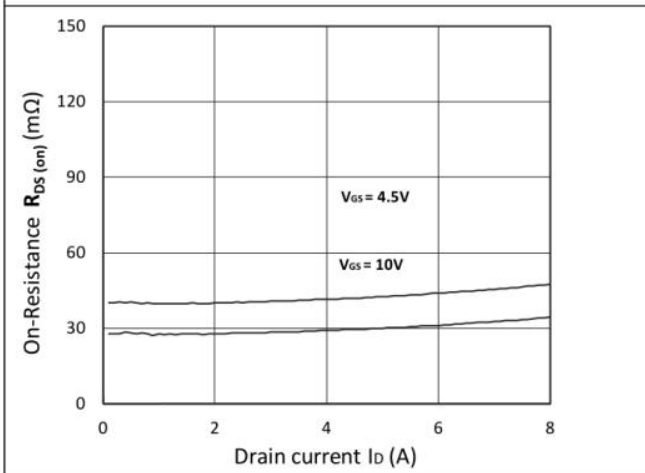


Figure 5.  $R_{DS(on)}$  vs.  $I_D$

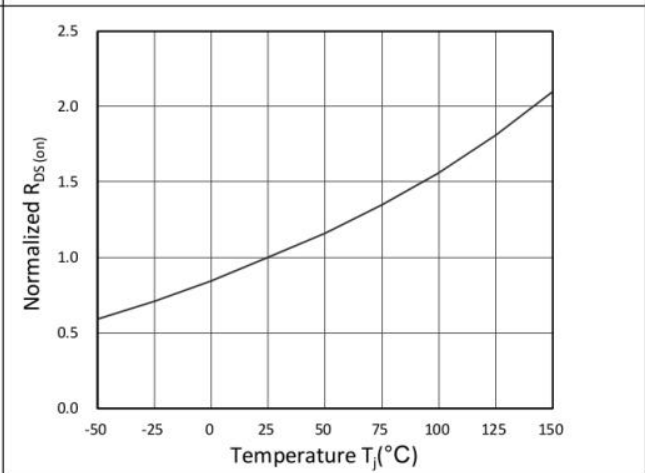


Figure 6. Normalized  $R_{DS(on)}$  vs. Temperature



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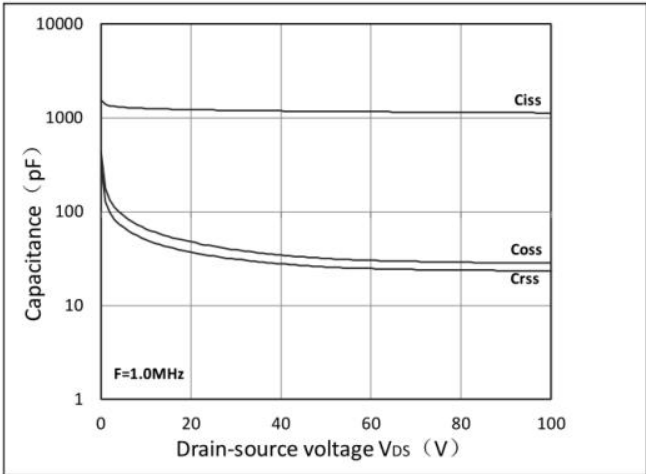


Figure 7. Capacitance Characteristics

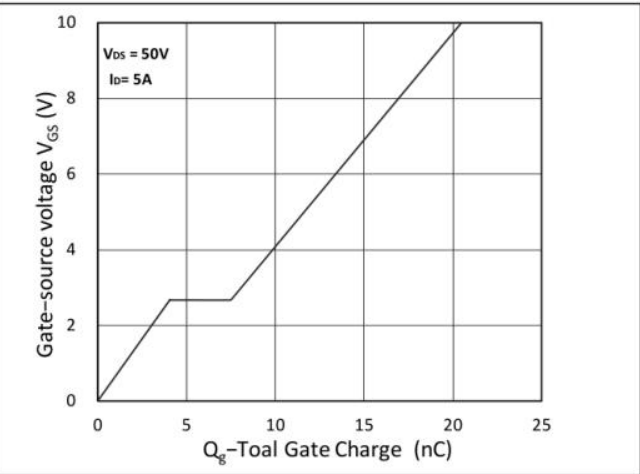


Figure 8. Gate Charge Characteristics

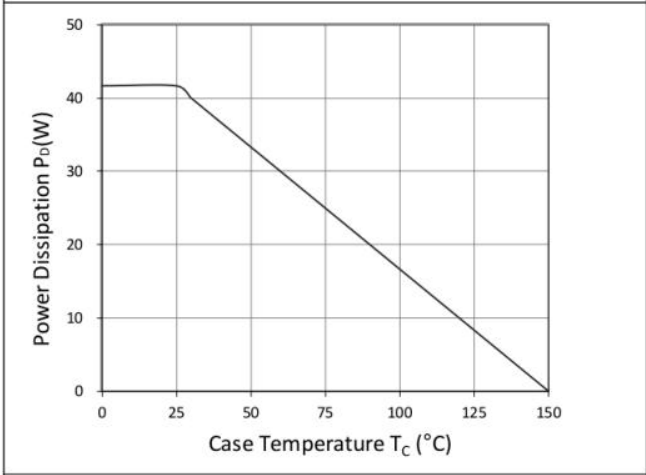


Figure 9. Power Dissipation

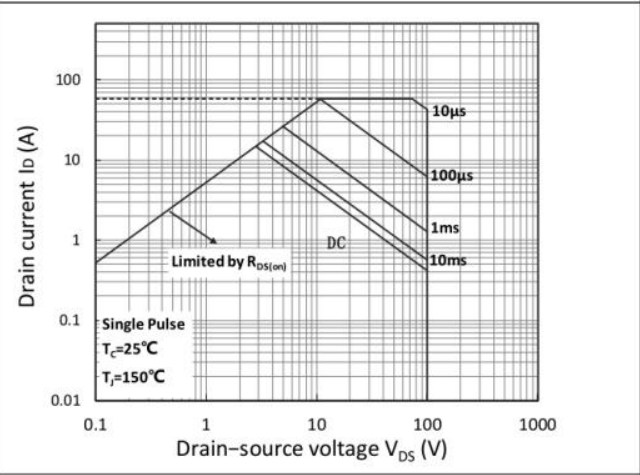


Figure 10. Safe Operating Area

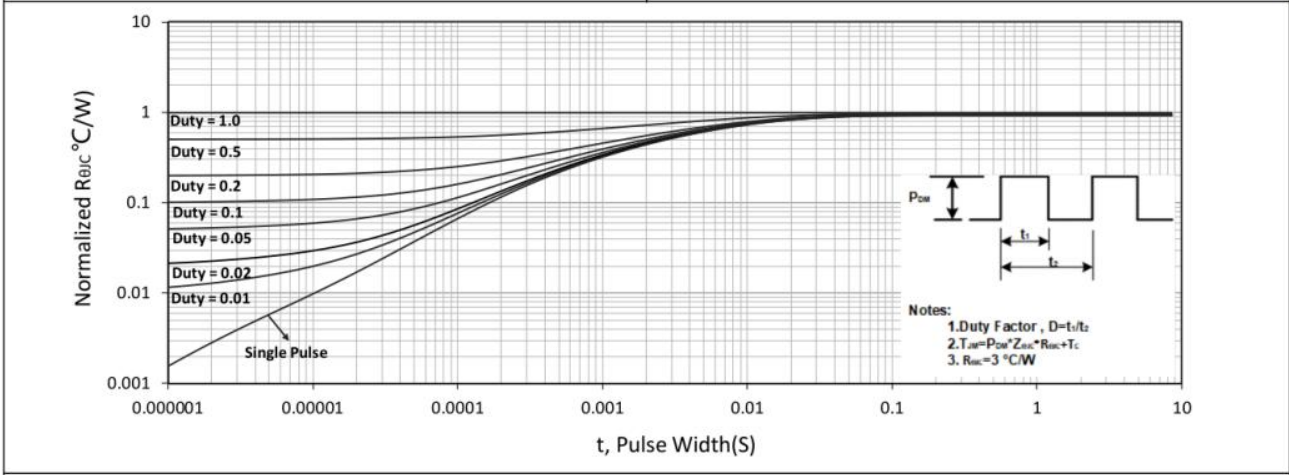
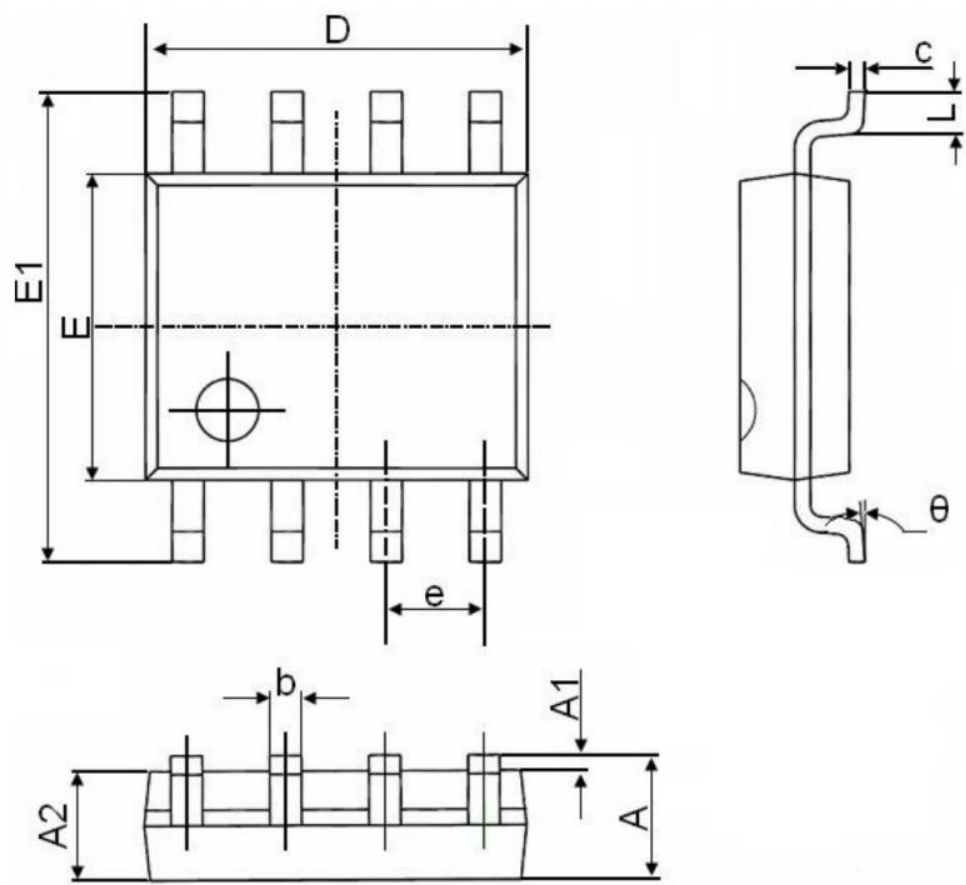


Figure 11. Normalized Maximum Transient Thermal Impedance

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SOP-8 Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270(BSC)		0.050(BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

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