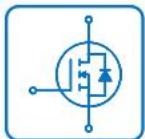




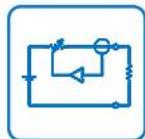
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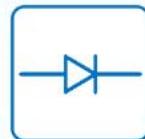
TVS



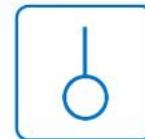
MOS



LDO



Diode



Sensor



DC-DC

Product Specification

▶ Domestic Part Number	IRF630N
▶ Overseas Part Number	IRF630N
▶ Equivalent Part Number	IRF630NSTRL



EV is the abbreviation of name EVVO

200V N-Channel Enhancement Mode MOSFET

Features

- Advanced Trench MOS Technology
- Low Gate Charge
- 100% EAS Guaranteed
- Green Device Available

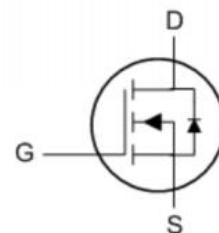
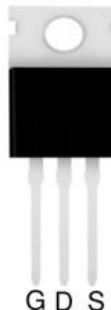
Applications

- Load Switch
- LED Applications
- Networking Applications
- Quick Charger

Product Summary

 $V_{DS} = 200V$ $I_D = 18A$ $R_{DS(ON)} < 170m\Omega$ @ $V_{GS} = 10V$ $R_{DS(ON)} < 180m\Omega$ @ $V_{GS} = 4.5V$

TO-220-3L Pin Configuration



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	200	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	18	A
$I_D @ T_C = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V^1$	11.7	A
I_{DM}	Pulsed Drain Current ²	40	A
EAS	Single Pulse Avalanche Energy ³	15	mJ
I_{AS}	Avalanche Current	10	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation ³	83	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-ambient ¹	---	60	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case ¹	---	1.1	°C/W

200V N-Channel Enhancement Mode MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

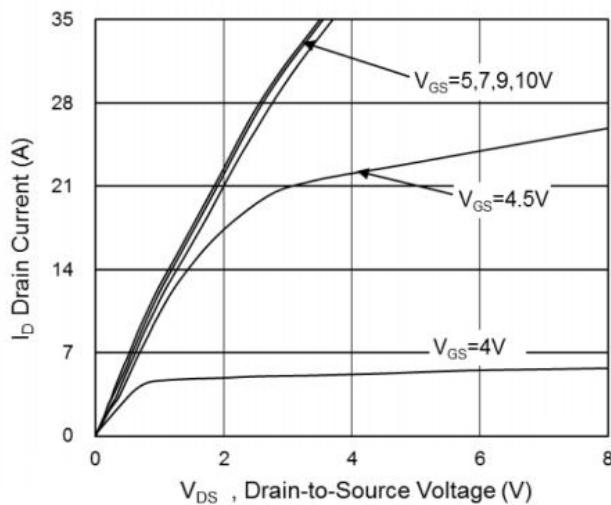
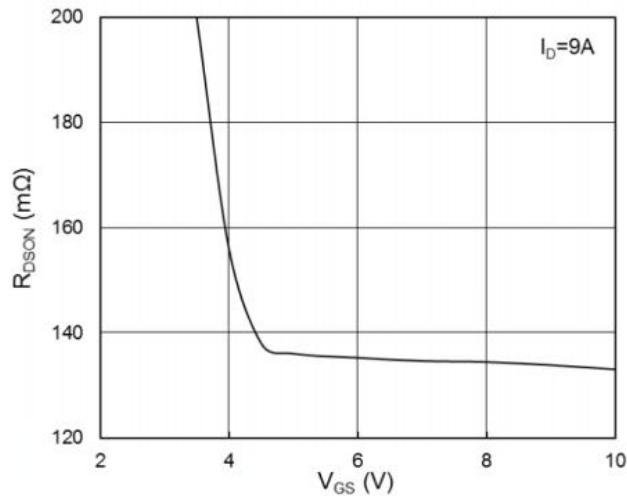
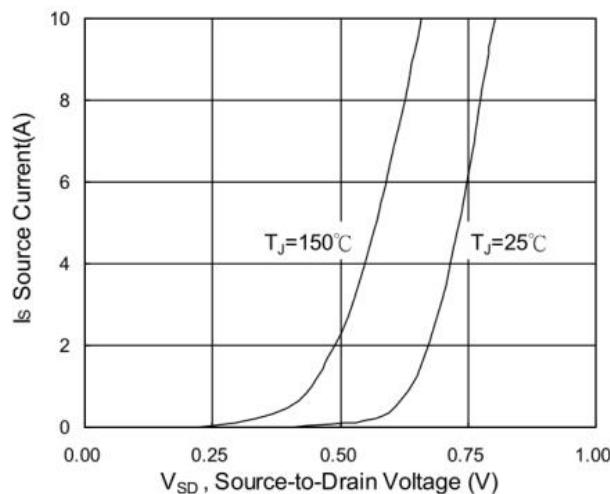
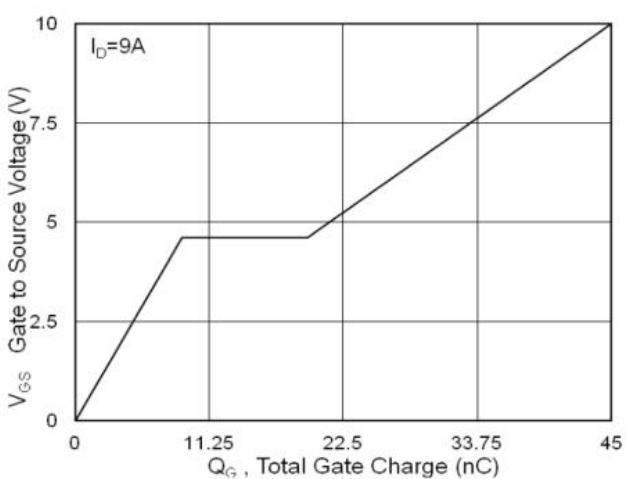
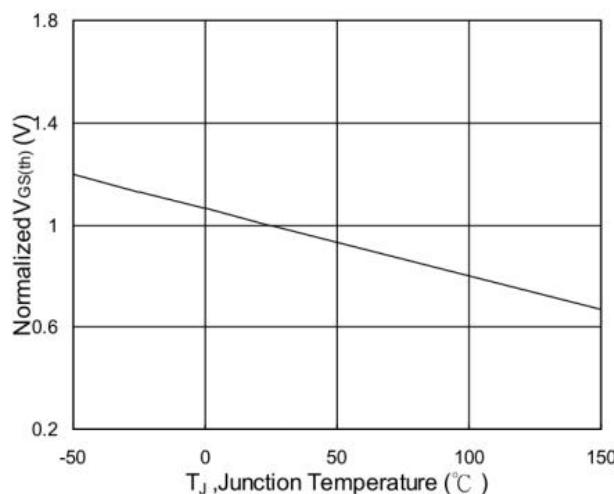
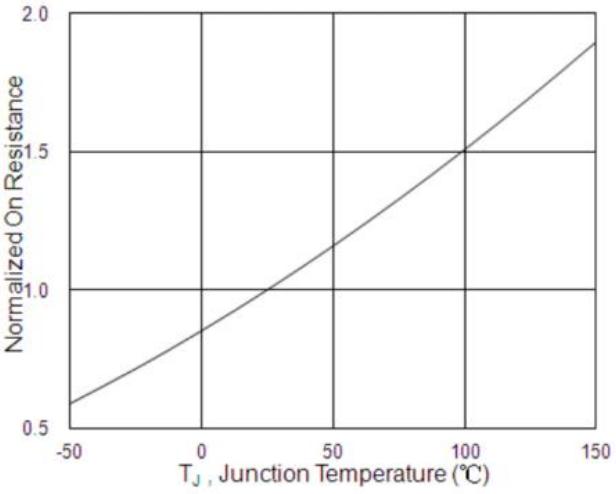
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	200	---	---	V
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=10\text{V}$, $I_D=9\text{A}$	---	---	170	$\text{m}\Omega$
	Static Drain-Source On-Resistance ²	$V_{\text{GS}}=4.5\text{V}$, $I_D=9\text{A}$	---	---	180	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1.2	---	2.5	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=160\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=160\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_D=9\text{A}$	---	22	---	S
R_g	Gate Resistance	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2	---	Ω
Q_g	Total Gate Charge (10V)	$V_{\text{DS}}=80\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=9\text{A}$	---	45	---	nC
Q_{gs}	Gate-Source Charge		---	9	---	
Q_{gd}	Gate-Drain Charge		---	10.5	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=3.3\Omega$	---	13	---	ns
T_r	Rise Time		---	8.2	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	25	---	
T_f	Fall Time		---	11	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	2047	---	pF
C_{oss}	Output Capacitance		---	109	---	
C_{rss}	Reverse Transfer Capacitance		---	70	---	

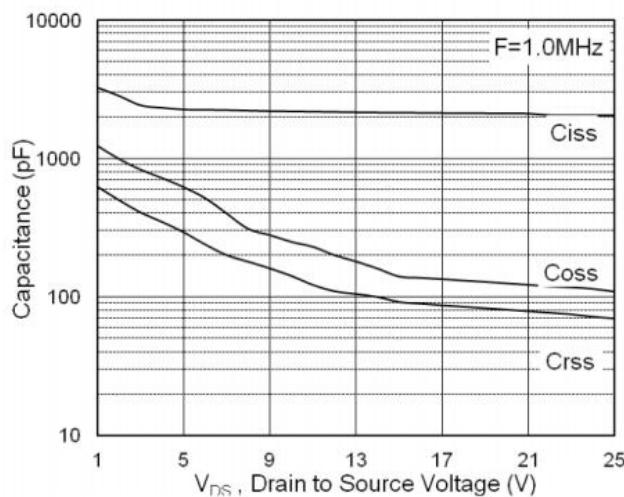
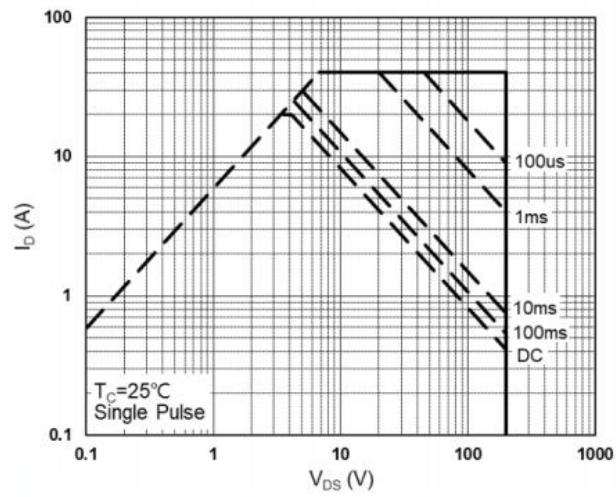
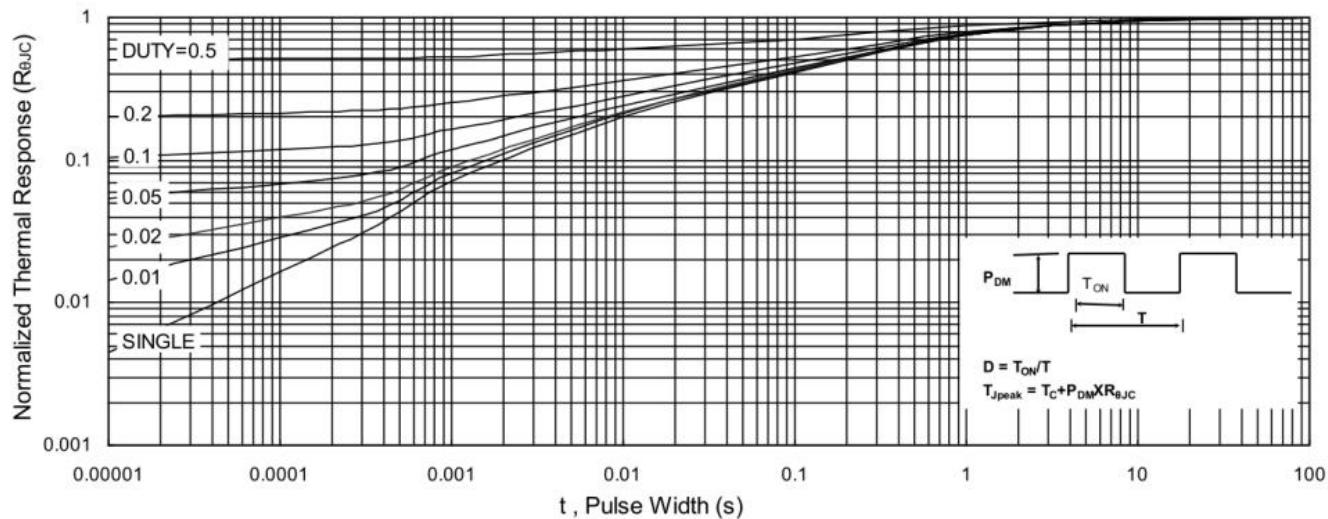
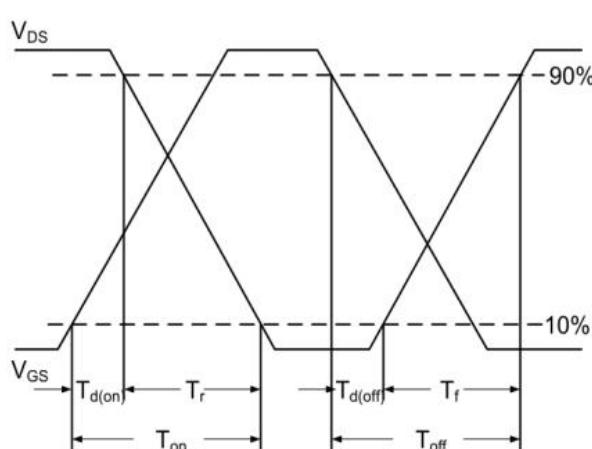
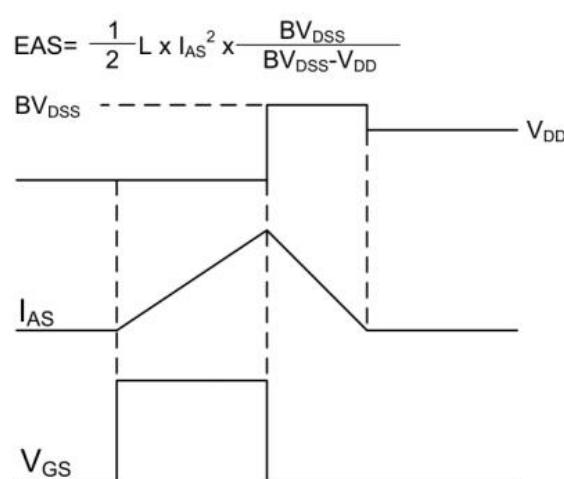
Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current ^{1,5}	$V_G=V_D=0\text{V}$, Force Current	---	---	18	A
I_{SM}	Pulsed Source Current ^{2,5}		---	---	40	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time	$I_F=10\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$,	---	37	---	nS
Q_{rr}	Reverse Recovery Charge	$T_J=25^\circ\text{C}$	---	103	---	nC

Note :

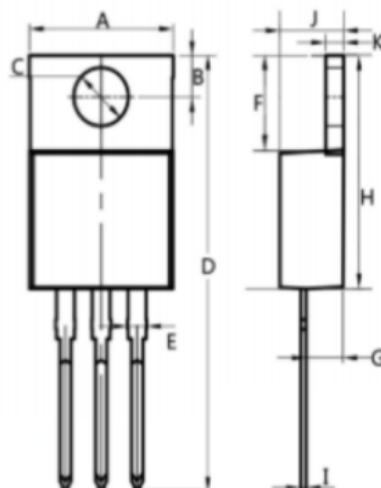
- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.3\text{mH}$, $I_{\text{AS}}=10\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

200V N-Channel Enhancement Mode MOSFET
Typical Characteristics
**Fig.1 Typical Output Characteristics****Fig.2 On-Resistance vs G-S Voltage****Fig.3 Source Drain Forward Characteristics****Fig.4 Gate-Charge Characteristics****Fig.5 Normalized $V_{GS(th)}$ vs T_J** **Fig.6 Normalized $R_{DS(on)}$ vs T_J**

200V N-Channel Enhancement Mode MOSFET
**Fig.7 Capacitance****Fig.8 Safe Operating Area****Fig.9 Normalized Maximum Transient Thermal Impedance****Fig.10 Switching Time Waveform****Fig.11 Unclamped Inductive Waveform**

200V N-Channel Enhancement Mode MOSFET

TO-220-3L



Dim.	Min.	Max.
A	10.0	10.4
B	2.5	3.0
C	3.5	4.0
D	28.0	30.0
E	1.1	1.5
F	6.2	6.6
G	2.9	3.3
H	15.0	16.0
I	0.35	0.45
J	4.3	4.7
K	1.2	1.4

All Dimensions in millimeter

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