

EVVOSEMI[®]

THINK CHANGE DO



ESD



TVS



MOS



LDO



Diode



Sensor



DC-DC

Product Specification

▶ Domestic	Part Number	STD35NF06L
▶ Overseas	Part Number	STD35NF06L
▶ Equivalent	Part Number	STD35NF06L

EV is the abbreviation of name EVVO

60V N-Channel Enhancement Mode Power MOSFET

General Description

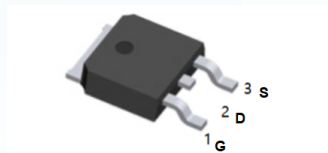
The STD35NF06L uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications.

Features

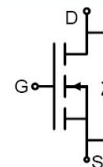
- $V_{DS} = 60V, I_D = 35A$
 $R_{DS(ON)}, 20m\Omega$ (Typ) @ $V_{GS} = 10V$
 $R_{DS(ON)}, 16m\Omega$ (Typ) @ $V_{GS} = 4.5V$
- Advanced Trench Technology
- Excellent $R_{DS(ON)}$ and Low Gate Charge
- Lead free product is acquired

Application

- Load Switch
- PWM Application
- Power management



TO-252(DPAK) top view



Schematic Diagram

Absolute Maximum Ratings($T_A = 25^\circ C$ unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	V
Drain Current-Continuous ^{Note3}	$TC = 25^\circ C$		35	A
	$TC = 100^\circ C$		25	A
Drain Current-Pulsed ^{Note1}		I_{DM}	140	A
Avalanche Energy ^{Note4}		E_{AS}	72	mJ
Maximum Power Dissipation	$TC = 25^\circ C$	P_D	105	W
Storage Temperature Range		T_{STG}	-55 to +150	$^\circ C$
Operating Junction Temperature Range		T_J	-55 to +150	$^\circ C$

Thermal Resistance

Parameter	Symbol	Min.	Typ.	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	-	-	1.4	$^\circ C/W$

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Electrical Characteristics(T_J=25℃ unless otherwise noted)

OFF CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _{DS} =250uA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1.0	uA
Gate-Body Leakage	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA

ON CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Gate Threshold Voltage	V _{GS(TH)}	V _{DS} =V _{GS} , I _{DS} =250uA	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _{DS} =30A	-	12	20	mΩ
		V _{GS} =4.5V, I _{DS} =20A	-	16		mΩ

DYNAMIC CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Input Capacitance	C _{iss}	V _{DS} =25V, V _{GS} = 0V, f=1MHz	-	1300	-	pF
Output Capacitance	C _{OSS}		-	300	-	
Reverse Transfer Capacitance	C _{rss}		-	105	-	

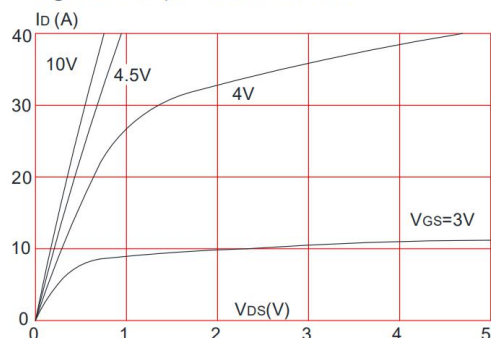
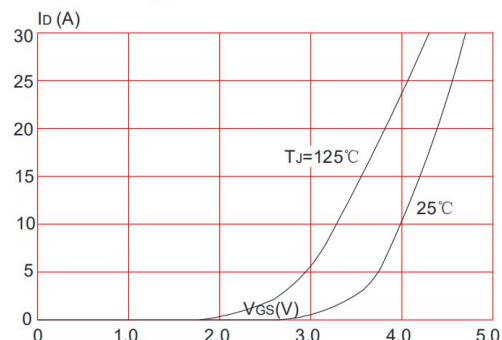
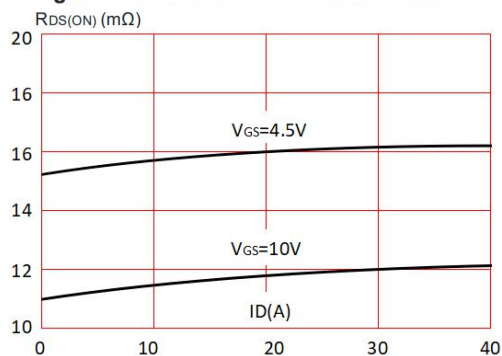
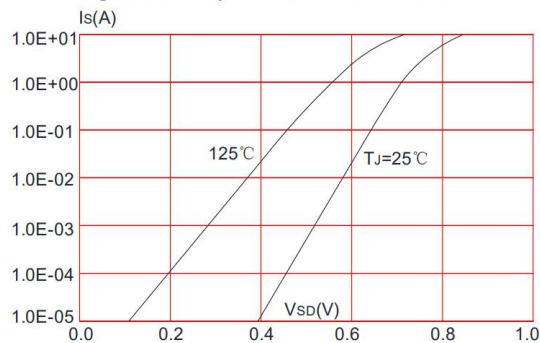
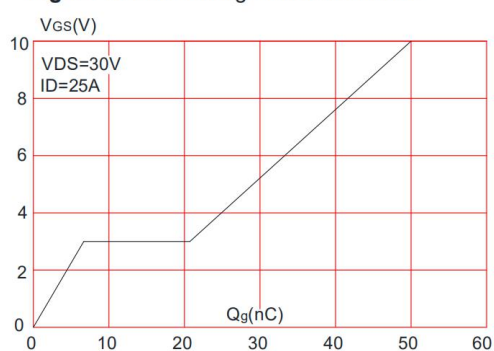
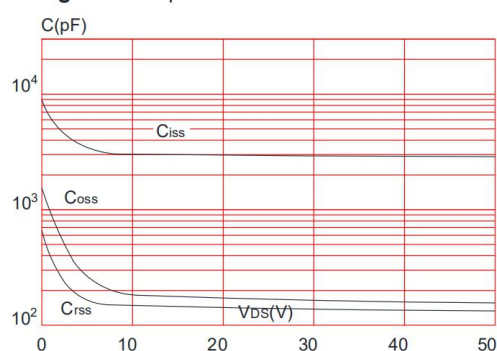
SWITCHING CHARACTERISTICS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Turn-On Delay Time	T _{d(on)}	V _{GS} =10V, V _{DS} =30V, R _{GEN} =4.7Ω I _D =27.5A	-	20	-	ns
Rise Time	t _r		-	50	-	
Turn-Off Delay Time	T _{d(off)}		-	36	-	
Fall Time	t _f		-	15	-	
Total Gate Charge at 10V	Q _g	V _{DS} =30V, I _{DS} =55A, V _{GS} =10V	-	44.5	60	nC
Gate to Source Gate Charge	Q _{gs}		-	10.5	-	
Gate to Drain"Miller"Charge	Q _{gd}		-	17.5	-	

DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS						
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _{DS} =35A	-	-	1.5	V
Reverse Recovery Time	t _{rr}	T _J =25℃, I _F =25A di/dt=100A/us	-	75	-	nS
Reverse Recovery Charge	Q _{rr}		-	170	-	uC

Notes:

- 1: Repetitive rating, pulse width limited by maximum junction temperature.
- 2: Surface mounted on FR4 Board, t≤10sec.
- 3: Pulse width ≤ 300μs, duty cycle ≤ 2%.
- 4: EAS condition: L=0.5mH, VDD=10V, V_G=10V, V_{GATE}=20V, Start T_J=25℃.

60V N-Channel Enhancement Mode Power MOSFET

Figure 1: Output Characteristics

Figure 2: Typical Transfer Characteristics

Figure 3: On-resistance vs. Drain Current

Figure 4: Body Diode Characteristics

Figure 5: Gate Charge Characteristics

Figure 6: Capacitance Characteristics


60V N-Channel Enhancement Mode Power MOSFET

Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

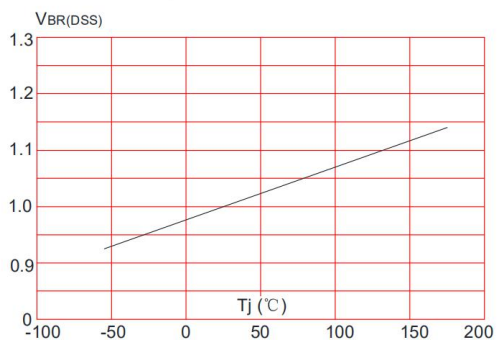


Figure 8: Normalized on Resistance vs. Junction Temperature

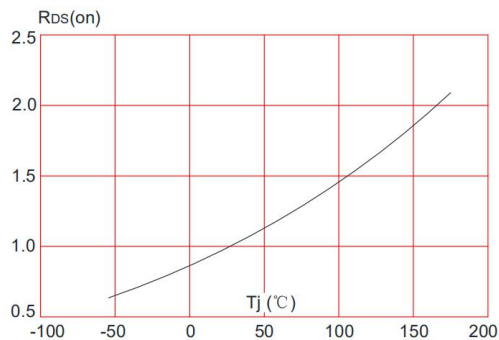


Figure 9: Maximum Safe Operating Area

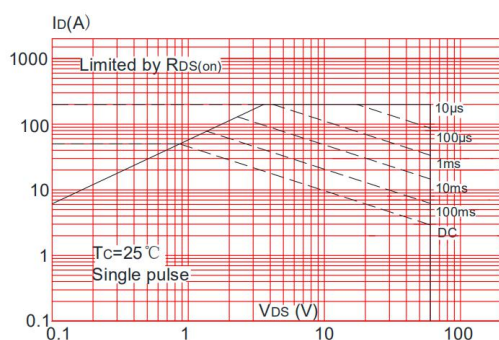


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

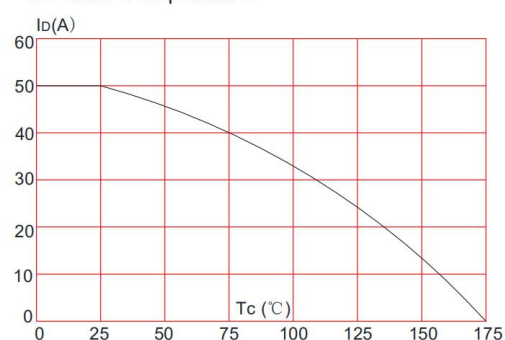
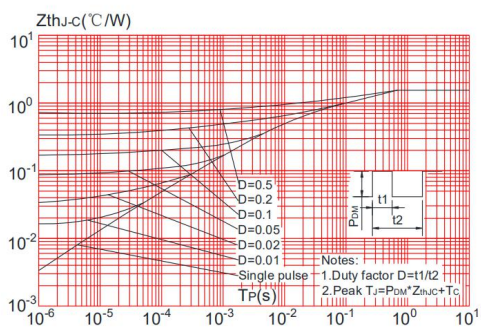


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case



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Test Circuit

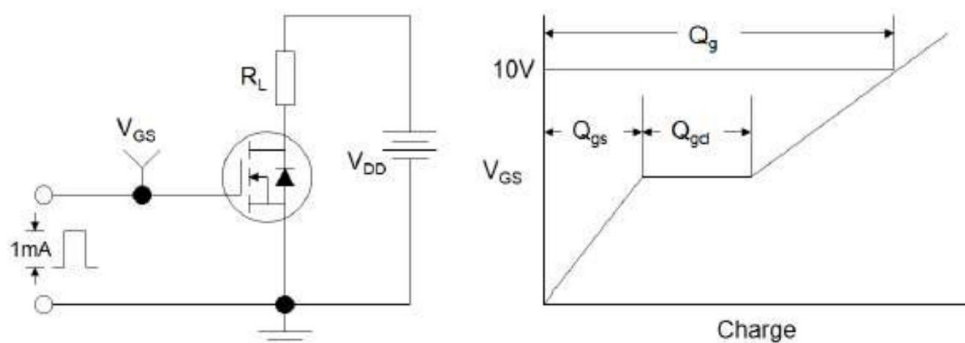


Figure1: Gate Charge Test Circuit & Waveform

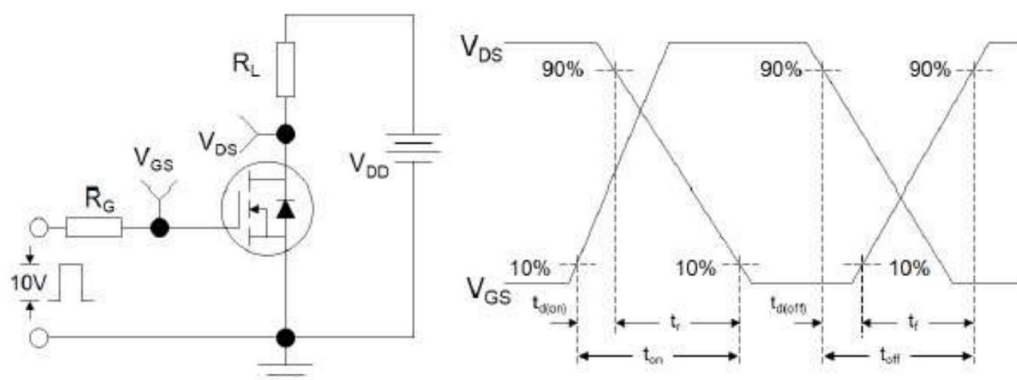


Figure 2: Resistive Switching Test Circuit & Waveforms

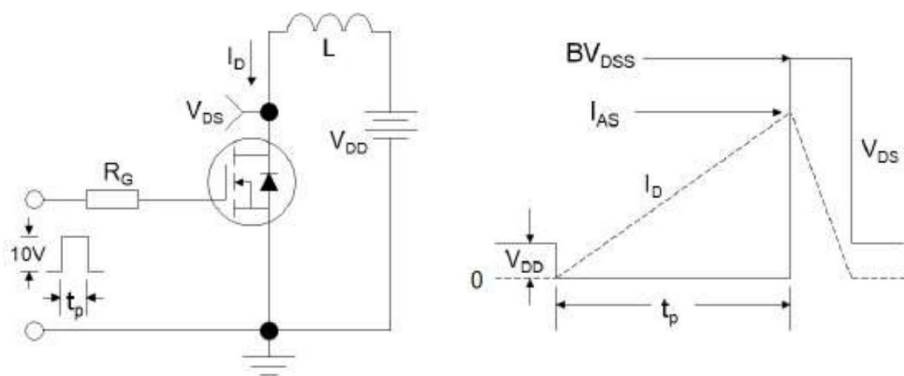
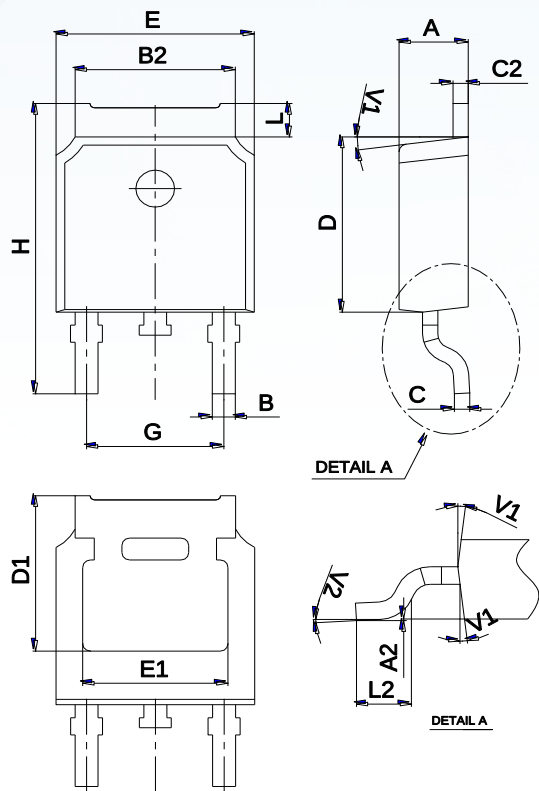


Figure 3: Unclamped Inductive Switching Test Circuit & Waveforms

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Package Mechanical Data TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Ordering information

Order code	Package	Baseqty	Delivery mode
STD35NF06L	TO-252	2500	Tape and reel

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