

EVVOSEMI[®]

THINK CHANGE DO



ESD



TVS



MOS



LDO



Diode



Sensor



DC-DC

Product Specification

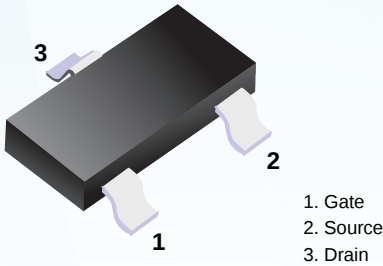
▶ Domestic	Part Number	AO3400A
▶ Overseas	Part Number	AO3400A
▶ Equivalent	Part Number	AO3400A

EV is the abbreviation of name EVVO

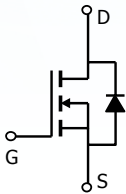
N-Channel Enhancement MOSFET

Features

- $V_{DS} (V) = 30V$
- $I_D = 5.8 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 28m \Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 33m \Omega (V_{GS} = 4.5V)$
- $R_{DS(ON)} < 52m \Omega (V_{GS} = 2.5V)$



Simplified outline(SOT23-3L)



Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current $T_A=25^{\circ}C$ $T_A=70^{\circ}C$	I_D	5.8 4.9	A
Pulsed Drain Current *	I_{DM}	30	
Power Dissipation $T_A=25^{\circ}C$ $T_A=70^{\circ}C$	P_D	1.4 1	W
Thermal Resistance.Junction- to-Ambient	R_{thJA}	125	$^{\circ}C/W$
Thermal Resistance.Junction- to-Case	R_{thc}	60	$^{\circ}C/W$
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^{\circ}C$

* Repetitive rating, pulse width limited by junction temperature.

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	I _D =250 μA, V _{GS} =0V	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V, V _{GS} =0V			1	μA
		V _{DS} =24V, V _{GS} =0V, T _J =55°C			5	
Gate-Body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V			±100	nA
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} I _D =250 μA	0.7	1.1	1.4	V
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =5.8A		22.8	28	mΩ
		V _{GS} =10V, I _D =5.8A T _J =125°C		32	39	
		V _{GS} =4.5V, I _D =5A		27.3	33	mΩ
		V _{GS} =2.5V, I _D =4A		43.3	52	mΩ
On state drain current	I _{D(ON)}	V _{GS} =4.5V, V _{DS} =5V	30			A
Forward Transconductance	g _{FS}	V _{DS} =5V, I _D =5A	10	15		S
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =15V, f=1MHz		823	1050	pF
Output Capacitance	C _{oss}			99		pF
Reverse Transfer Capacitance	C _{rss}			77		pF
Gate resistance	R _g	V _{GS} =0V, V _{DS} =0V, f=1MHz		1.4	3.6	Ω
Total Gate Charge	Q _g	V _{GS} =4.5V, V _{DS} =15V, I _D =5.8A		9.7	12	nC
Gate Source Charge	Q _{gs}			1.6		nC
Gate Drain Charge	Q _{gd}			3.1		nC
Turn-On DelayTime	t _{D(on)}	V _{GS} =10V, V _{DS} =15V, R _L =2.7 Ω, R _{GEN} =3 Ω		3.3	5	ns
Turn-On Rise Time	t _r			4.8	7	ns
Turn-Off DelayTime	t _{D(off)}			26.3	40	ns
Turn-Off Fall Time	t _f			4.1	6	ns
Body Diode Reverse Recovery Time	t _{rr}	I _F =5A, dI/dt=100A/μs		16	20	ns
Body Diode Reverse Recovery Charge	Q _{rr}	I _F =5A, dI/dt=100A/μs		8.9	12	nC
Maximum Body-Diode Continuous Current	I _S				2.5	A
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V		0.71	1	V

■ Typical Characteristics

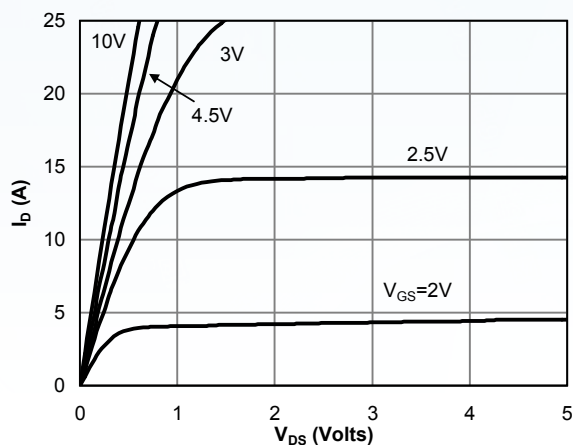


Fig 1: On-Region Characteristics

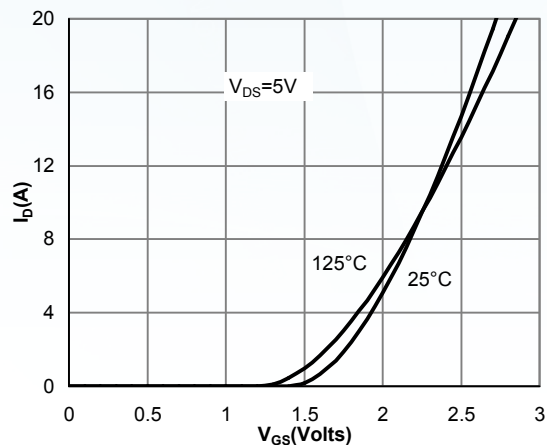


Figure 2: Transfer Characteristics

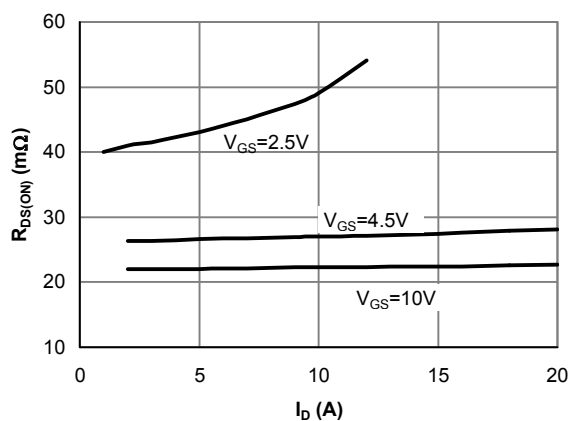


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

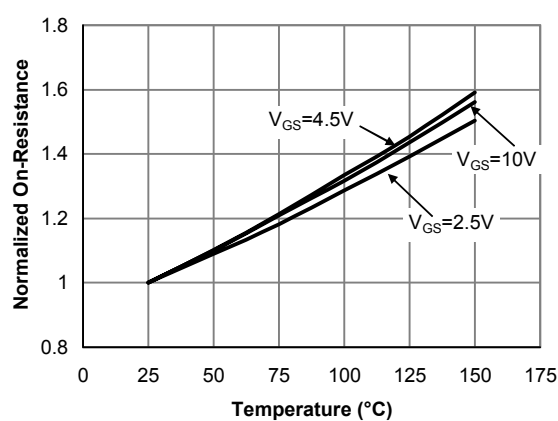


Figure 4: On-Resistance vs. Junction Temperature

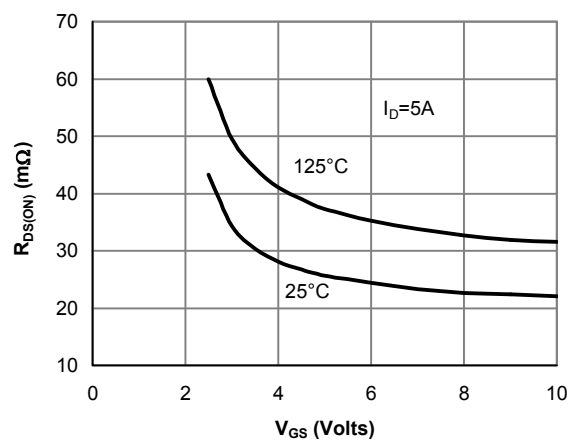


Figure 5: On-Resistance vs. Gate-Source Voltage

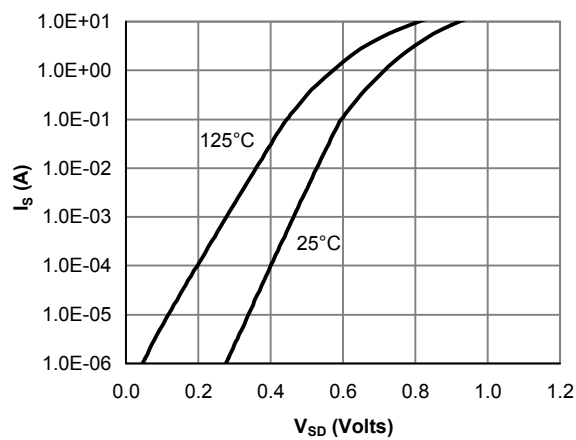


Figure 6: Body-Diode Characteristics

■ Typical Characteristics

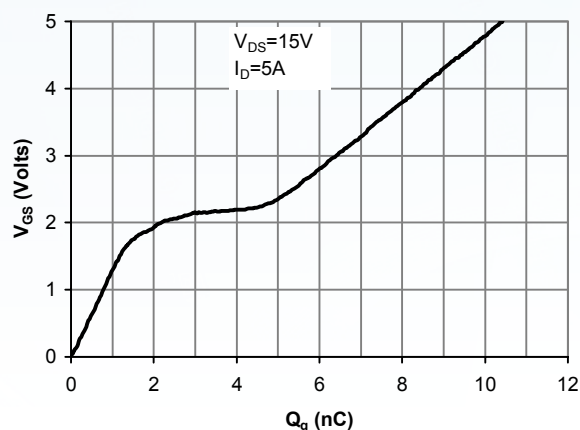


Figure 7: Gate-Charge Characteristics

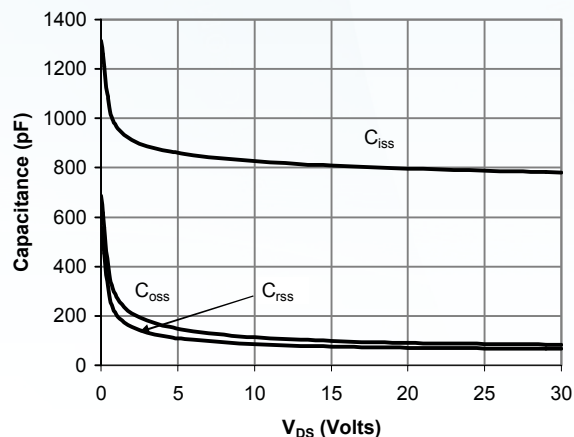


Figure 8: Capacitance Characteristics

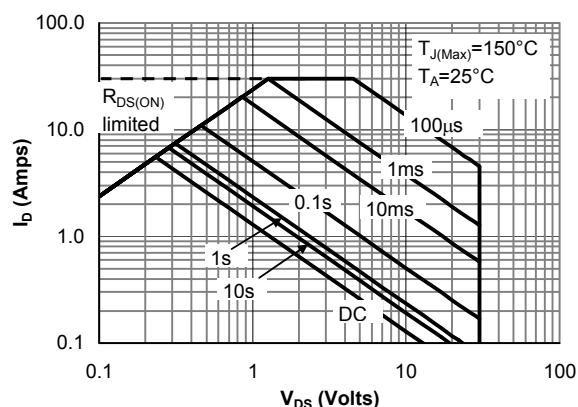


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

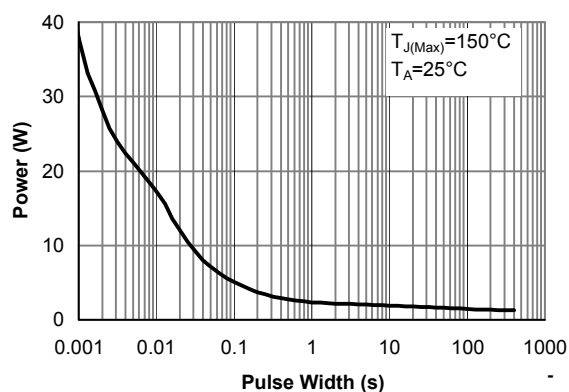


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

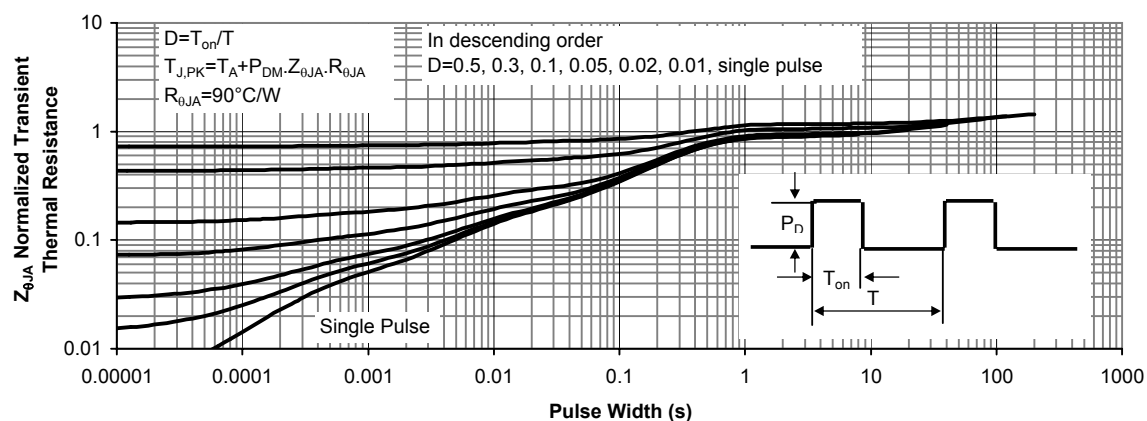
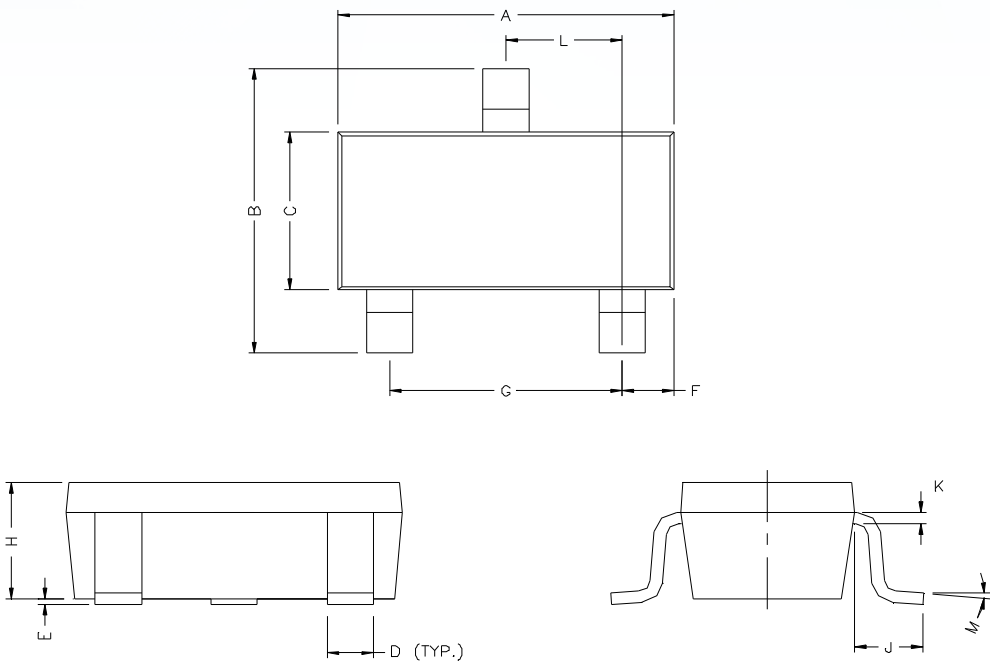


Figure 11: Normalized Maximum Transient Thermal Impedance

■ SOT23-3L



DIMENSIONS (mm are the original dimensions)

UNIT	A	B	C	D	E	F	G	H	K	J	L	M
mm	2.70 3.10	2.65 2.95	1.50 1.70	0.35 0.50	0 0.10	0.45 0.55	1.9	1.00 1.30	0.10 0.20	0.40 -	0.85 1.15	0° 10°

Disclaimer

EVVOSEMI ("EVVO") reserves the right to make corrections, enhancements, improvements, and other changes to its products and services at any time, and to discontinue any product or service without notice.

EVVO warrants the performance of its hardware products to the specifications applicable at the time of sale in accordance with its standard warranty. Testing and other quality control techniques are used as deemed necessary by EVVO to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

Customers should obtain and confirm the latest product information and specifications before final design, purchase, or use. EVVO makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does EVVO assume any liability for application assistance or customer product design. EVVO does not warrant or accept any liability for products that are purchased or used for any unintended or unauthorized application.

EVVO products are not authorized for use as critical components in life support devices or systems without the express written approval of EVVOSEMI.

The EVVO logo and EVVOSEMI are trademarks of EVVOSEMI or its subsidiaries in relevant jurisdictions. EVVO reserves the right to make changes without further notice to any products herein.