

# EVVOSEMI<sup>®</sup>

THINK CHANGE DO



ESD



TVS



MOS



LDO



Diode



Sensor



DC-DC

## Product Specification

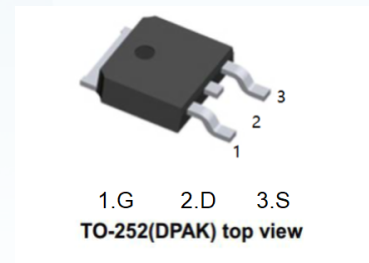
|              |             |            |
|--------------|-------------|------------|
| ▶ Domestic   | Part Number | FQD17P06TM |
| ▶ Overseas   | Part Number | FQD17P06TM |
| ▶ Equivalent | Part Number | FQD17P06TM |

EV is the abbreviation of name EVVO

## -60V P-Channel MOSFET

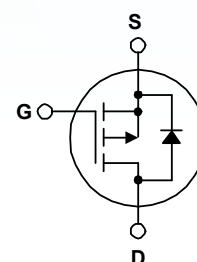
### Description

This advanced MOSFET technology has been especially tailored to reduce on-state resistance, and to provide superior switching performance and high avalanche energy strength. These devices are suitable for switched mode power supplies, audio amplifier, DC motor control, and variable switching power applications.



### Features

- $V_{DS}$  (V) = -60V
- $I_D$  = -12A ( $V_{GS}$  = -10V)
- $R_{DS(ON)}$  < 135m $\Omega$  ( $V_{GS}$  = -10V)



### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted.

| Symbol                            | Parameter                                                                      |                                       | FQD17P06 / FQU17P06 | Unit |
|-----------------------------------|--------------------------------------------------------------------------------|---------------------------------------|---------------------|------|
| V <sub>DSS</sub>                  | Drain-Source Voltage                                                           |                                       | -60                 | V    |
| I <sub>D</sub>                    | Drain Current                                                                  | - Continuous (T <sub>C</sub> = 25°C)  | -12                 | A    |
|                                   |                                                                                | - Continuous (T <sub>C</sub> = 100°C) | -7.6                | A    |
| I <sub>DM</sub>                   | Drain Current                                                                  | - Pulsed (Note 1)                     | -48                 | A    |
| V <sub>GSS</sub>                  | Gate-Source Voltage                                                            |                                       | ± 25                | V    |
| E <sub>AS</sub>                   | Single Pulsed Avalanche Energy (Note 2)                                        |                                       | 300                 | mJ   |
| I <sub>AR</sub>                   | Avalanche Current (Note 1)                                                     |                                       | -12                 | A    |
| E <sub>AR</sub>                   | Repetitive Avalanche Energy (Note 1)                                           |                                       | 4.4                 | mJ   |
| dv/dt                             | Peak Diode Recovery dv/dt (Note 3)                                             |                                       | -7.0                | V/ns |
| P <sub>D</sub>                    | Power Dissipation (T <sub>A</sub> = 25°C) *                                    |                                       | 2.5                 | W    |
|                                   | Power Dissipation (T <sub>C</sub> = 25°C)                                      |                                       | 44                  | W    |
|                                   |                                                                                | - Derate above 25°C                   | 0.35                | W/°C |
| T <sub>J</sub> , T <sub>STG</sub> | Operating and Storage Temperature Range                                        |                                       | -55 to +150         | °C   |
| T <sub>L</sub>                    | Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds. |                                       | 300                 | °C   |

### Thermal Characteristics

| Symbol          | Parameter                                                                             | FQD17P06 | Unit                      |
|-----------------|---------------------------------------------------------------------------------------|----------|---------------------------|
| $R_{\theta JC}$ | Thermal Resistance, Junction-to-Case, Max.                                            | 2.85     | $^\circ\text{C}/\text{W}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient (Minimum Pad of 2-oz Copper), Max.            | 110      |                           |
| $R_{\theta JA}$ | Thermal Resistance, Junction to Ambient (*1 in <sup>2</sup> Pad of 2-oz Copper), Max. | 50       |                           |

\* When mounted on the minimum pad size recommended (PCB Mount)

## -60V P-Channel MOSFET

**Electrical Characteristics**  $T_C = 25^\circ\text{C}$  unless otherwise noted.

| Symbol                                                 | Parameter                                             | Test Conditions                                                          | Min.     | Typ.  | Max. | Unit |
|--------------------------------------------------------|-------------------------------------------------------|--------------------------------------------------------------------------|----------|-------|------|------|
| Off Characteristics                                    |                                                       |                                                                          |          |       |      |      |
| BV <sub>DSS</sub>                                      | Drain-Source Breakdown Voltage                        | V <sub>GS</sub> = 0 V, I <sub>D</sub> = -250 μA                          | -60      |       |      | V    |
| ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>                   | Breakdown Voltage Temperature Coefficient             | I <sub>D</sub> = -250 μA, Referenced to 25°C                             |          | -0.06 |      | V/°C |
| I <sub>DSS</sub>                                       | Zero Gate Voltage Drain Current                       | V <sub>DS</sub> = -60 V, V <sub>GS</sub> = 0 V                           |          |       | -1   | μA   |
|                                                        |                                                       | V <sub>DS</sub> = -48 V, T <sub>C</sub> = 125°C                          |          |       | -10  | μA   |
| I <sub>GSSF</sub>                                      | Gate-Body Leakage Current, Forward                    | V <sub>GS</sub> = -25 V, V <sub>DS</sub> = 0 V                           |          |       | -100 | nA   |
| I <sub>GSSR</sub>                                      | Gate-Body Leakage Current, Reverse                    | V <sub>GS</sub> = 25 V, V <sub>DS</sub> = 0 V                            |          |       | 100  | nA   |
| On Characteristics                                     |                                                       |                                                                          |          |       |      |      |
| V <sub>GS(th)</sub>                                    | Gate Threshold Voltage                                | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA             | -1.1     | -2    | -3.0 | V    |
| R <sub>DS(on)</sub>                                    | Static Drain-Source On-Resistance                     | V <sub>GS</sub> = -10 V, I <sub>D</sub> = -6.0 A                         |          | 110   | 135  | mΩ   |
| g <sub>FS</sub>                                        | Forward Transconductance                              | V <sub>DS</sub> = -30 V, I <sub>D</sub> = -6.0 A                         |          | 8.7   |      | S    |
| Dynamic Characteristics                                |                                                       |                                                                          |          |       |      |      |
| C <sub>iss</sub>                                       | Input Capacitance                                     | V <sub>DS</sub> = -25 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz              |          | 690   | 900  | pF   |
| C <sub>oss</sub>                                       | Output Capacitance                                    |                                                                          |          | 325   | 420  | pF   |
| C <sub>rss</sub>                                       | Reverse Transfer Capacitance                          |                                                                          |          | 80    | 105  | pF   |
| Switching Characteristics                              |                                                       |                                                                          |          |       |      |      |
| t <sub>d(on)</sub>                                     | Turn-On Delay Time                                    | V <sub>DD</sub> = -30 V, I <sub>D</sub> = -8.5 A, R <sub>G</sub> = 25 Ω  |          | 13    | 35   | ns   |
| t <sub>r</sub>                                         | Turn-On Rise Time                                     |                                                                          |          | 100   | 210  | ns   |
| t <sub>d(off)</sub>                                    | Turn-Off Delay Time                                   |                                                                          |          | 22    | 55   | ns   |
| t <sub>f</sub>                                         | Turn-Off Fall Time                                    |                                                                          | (Note 4) | 60    | 130  | ns   |
| Q <sub>g</sub>                                         | Total Gate Charge                                     | V <sub>DS</sub> = -48 V, I <sub>D</sub> = -17 A, V <sub>GS</sub> = -10 V |          | 21    | 27   | nC   |
| Q <sub>gs</sub>                                        | Gate-Source Charge                                    |                                                                          |          | 4.2   |      | nC   |
| Q <sub>gd</sub>                                        | Gate-Drain Charge                                     |                                                                          | (Note 4) | 10    |      | nC   |
| Drain-Source Diode Characteristics and Maximum Ratings |                                                       |                                                                          |          |       |      |      |
| I <sub>S</sub>                                         | Maximum Continuous Drain-Source Diode Forward Current |                                                                          |          |       | -12  | A    |
| I <sub>SM</sub>                                        | Maximum Pulsed Drain-Source Diode Forward Current     |                                                                          |          |       | -48  | A    |
| V <sub>SD</sub>                                        | Drain-Source Diode Forward Voltage                    | V <sub>GS</sub> = 0 V, I <sub>S</sub> = -12 A                            |          |       | -4.0 | V    |
| t <sub>rr</sub>                                        | Reverse Recovery Time                                 | V <sub>GS</sub> = 0 V, I <sub>S</sub> = -17 A,                           |          | 92    |      | ns   |
| Q <sub>rr</sub>                                        | Reverse Recovery Charge                               | dl <sub>F</sub> / dt = 100 A/μs                                          |          | 0.32  |      | μC   |

**NOTES:**

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2.  $L = 2.4\text{ mH}$ ,  $I_{AS} = -12\text{ A}$ ,  $V_{DD} = -25\text{ V}$ ,  $R_G = 25\text{ }\Omega$ , starting  $T_J = 25^\circ\text{C}$ .
3.  $I_{SD} \leq -17\text{ A}$ ,  $di/dt \leq 300\text{ A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , starting  $T_J = 25^\circ\text{C}$ .
4. Essentially independent of operating temperature typical characteristics.

## -60V P-Channel MOSFET

### Typical Performance Characteristics

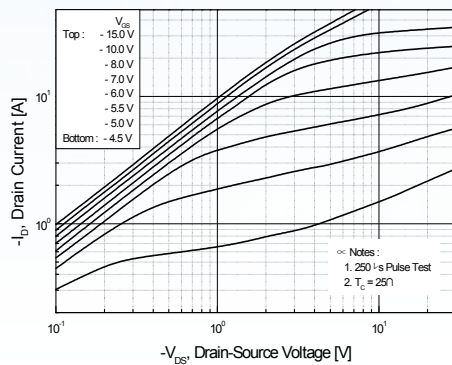


Figure 1. On-Region Characteristics

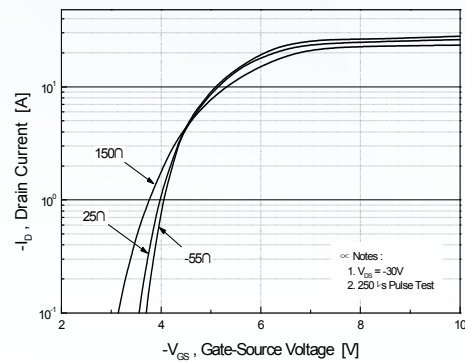


Figure 2. Transfer Characteristics

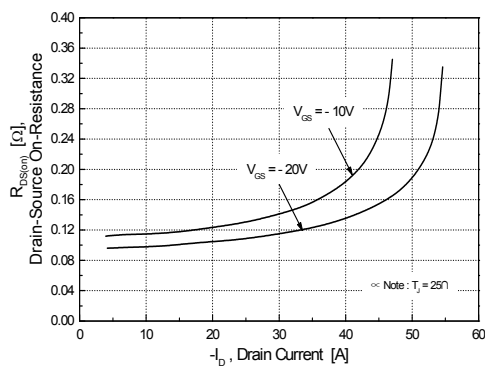


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

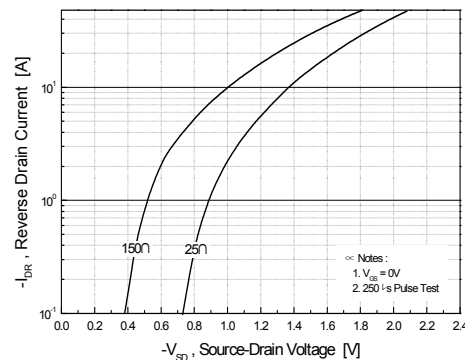


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

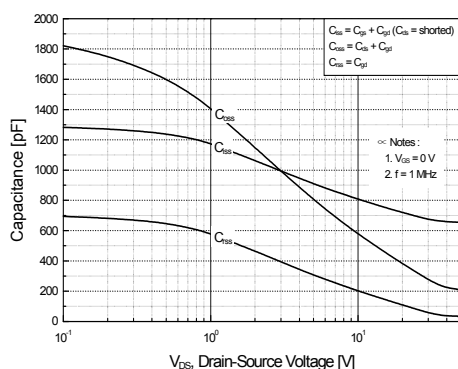


Figure 5. Capacitance Characteristics

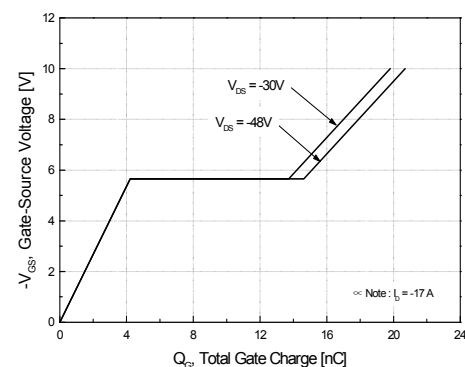


Figure 6. Gate Charge Characteristics

## -60V P-Channel MOSFET

### Typical Performance Characteristics

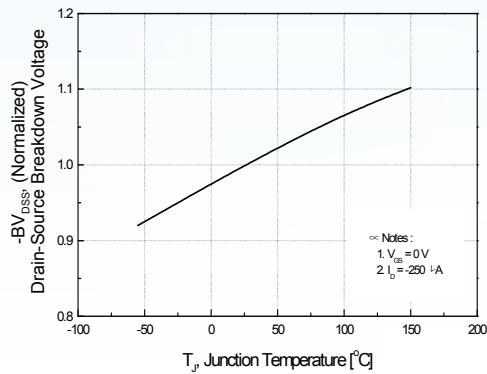


Figure 7. Breakdown Voltage Variation vs. Temperature

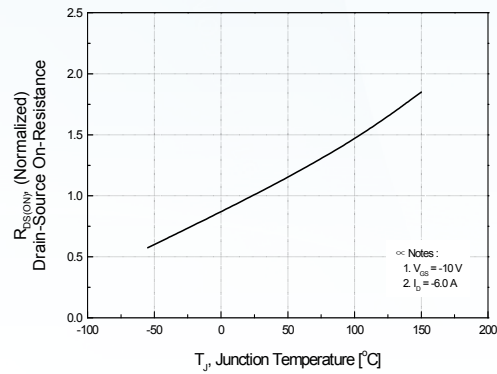


Figure 8. On-Resistance Variation vs. Temperature

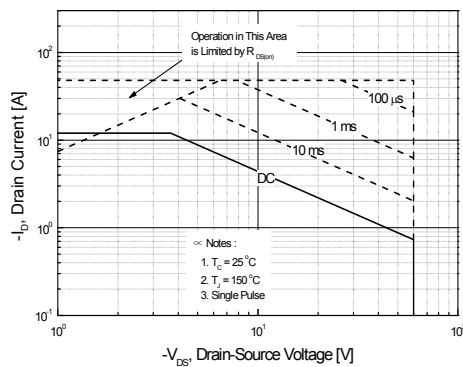


Figure 9. Maximum Safe Operating Area

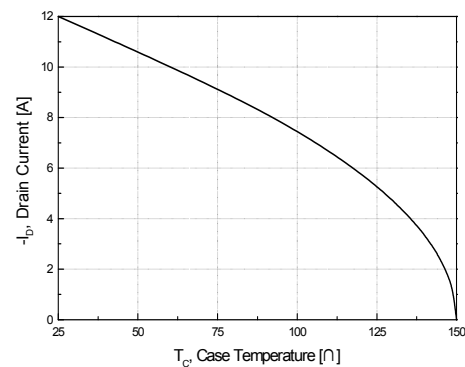


Figure 10. Maximum Drain Current vs. Case Temperature

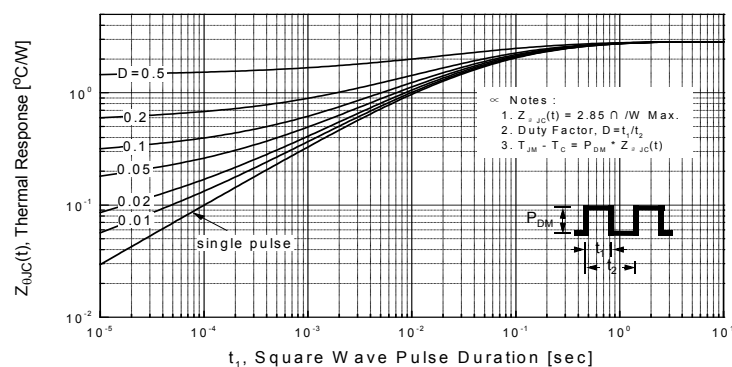


Figure 11. Transient Thermal Response Curve

-60V P-Channel MOSFET

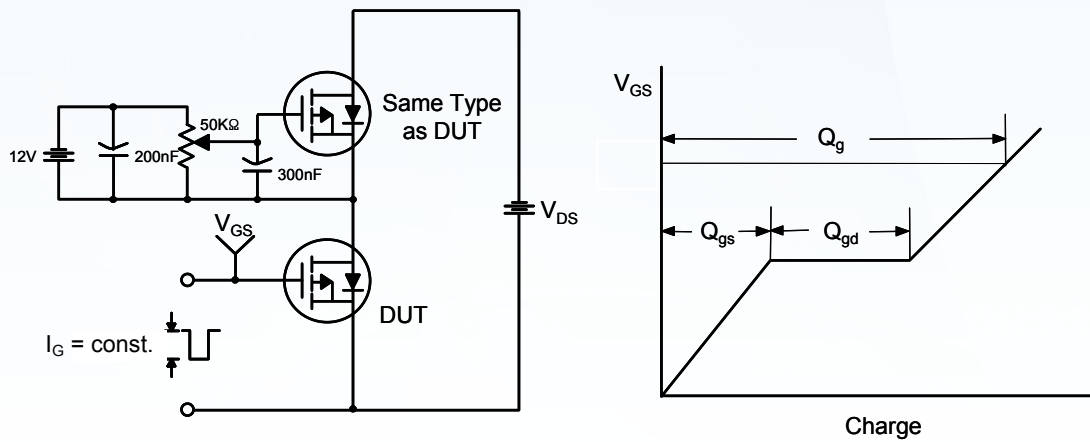


Figure 12. Gate Charge Test Circuit & Waveform

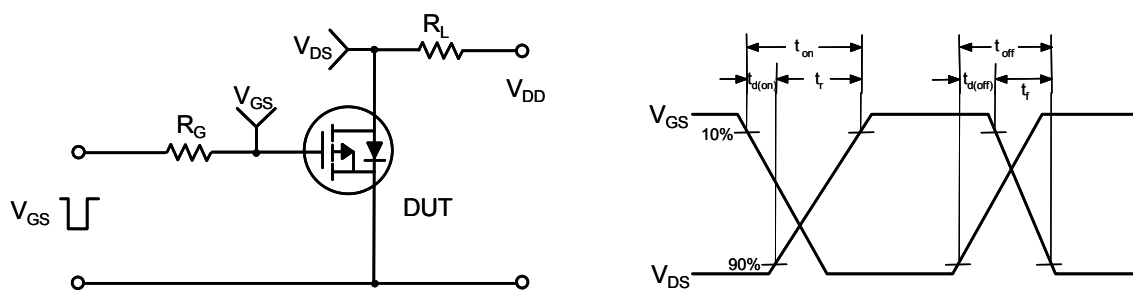


Figure 13. Resistive Switching Test Circuit & Waveforms

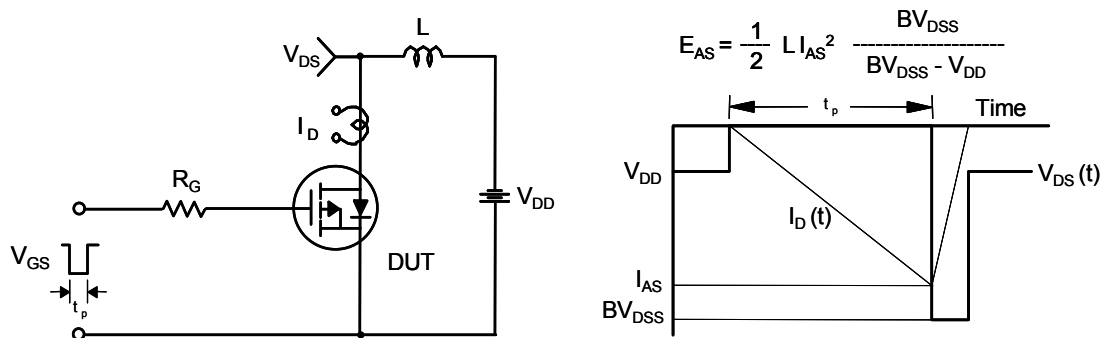
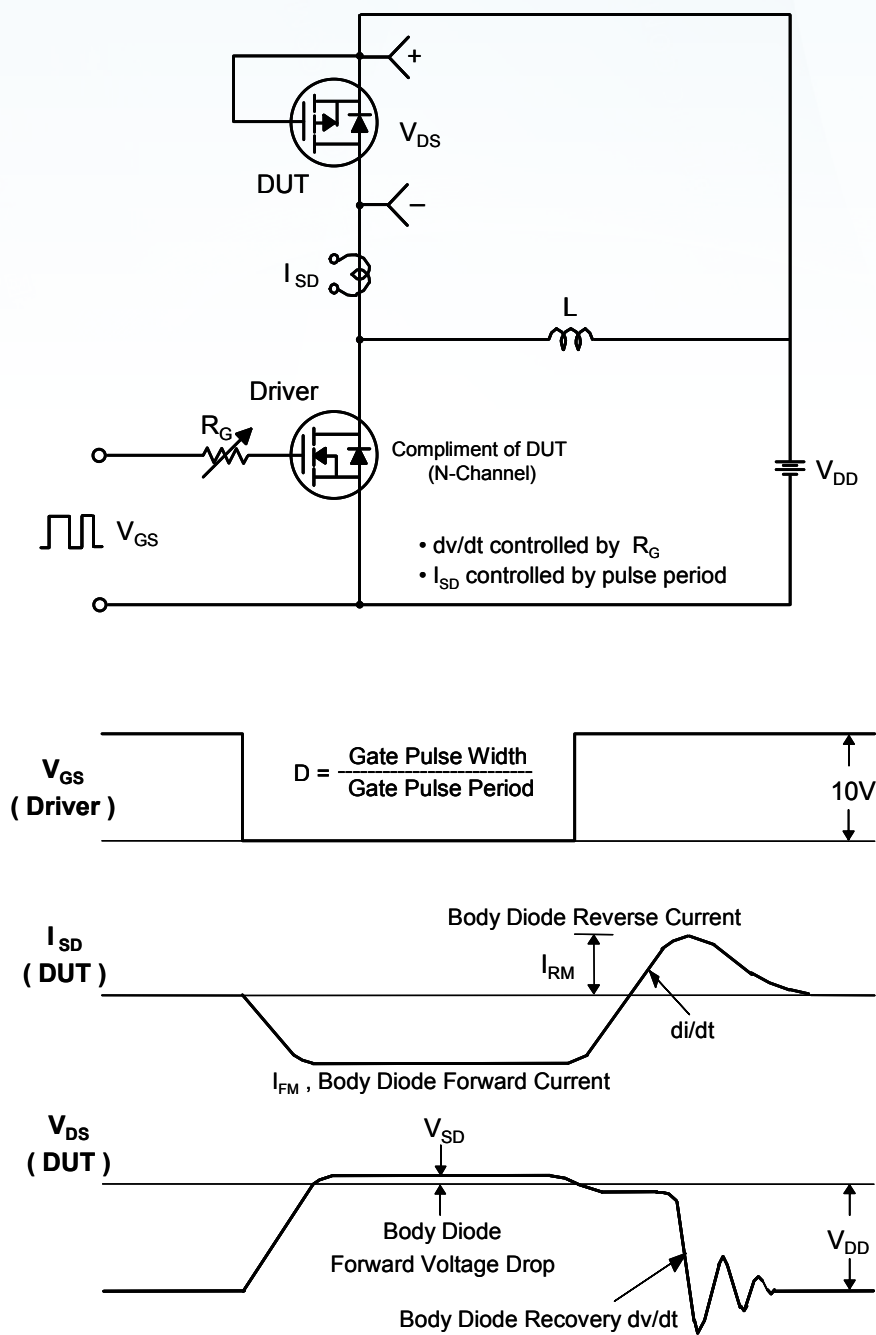


Figure 14. Unclamped Inductive Switching Test Circuit & Waveforms



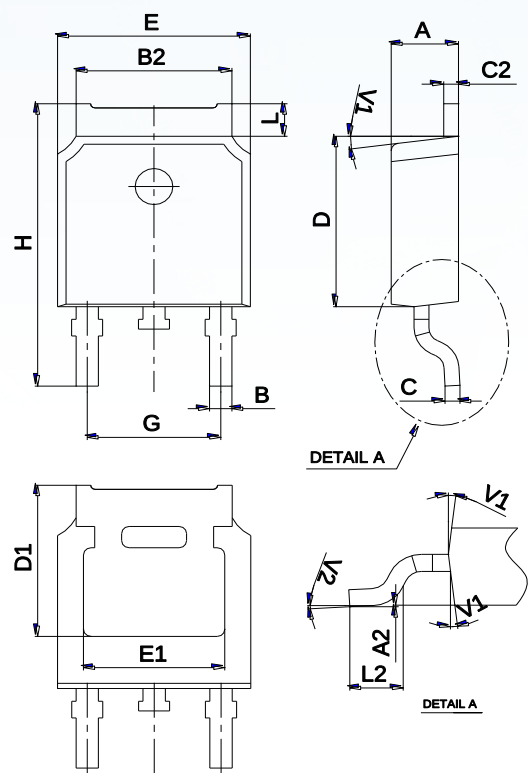
## -60V P-Channel MOSFET



### Figure 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms

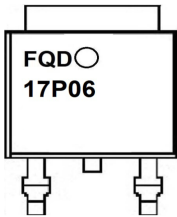
-60V P-Channel MOSFET

Package Mechanical Data TO-252



| Ref. | Dimensions  |      |       |          |      |       |
|------|-------------|------|-------|----------|------|-------|
|      | Millimeters |      |       | Inches   |      |       |
|      | Min.        | Typ. | Max.  | Min.     | Typ. | Max.  |
| A    | 2.10        |      | 2.50  | 0.083    |      | 0.098 |
| A2   | 0           |      | 0.10  | 0        |      | 0.004 |
| B    | 0.66        |      | 0.86  | 0.026    |      | 0.034 |
| B2   | 5.18        |      | 5.48  | 0.202    |      | 0.216 |
| C    | 0.40        |      | 0.60  | 0.016    |      | 0.024 |
| C2   | 0.44        |      | 0.58  | 0.017    |      | 0.023 |
| D    | 5.90        |      | 6.30  | 0.232    |      | 0.248 |
| D1   | 5.30REF     |      |       | 0.209REF |      |       |
| E    | 6.40        |      | 6.80  | 0.252    |      | 0.268 |
| E1   | 4.63        |      |       | 0.182    |      |       |
| G    | 4.47        |      | 4.67  | 0.176    |      | 0.184 |
| H    | 9.50        |      | 10.70 | 0.374    |      | 0.421 |
| L    | 1.09        |      | 1.21  | 0.043    |      | 0.048 |
| L2   | 1.35        |      | 1.65  | 0.053    |      | 0.065 |
| V1   |             | 7°   |       |          | 7°   |       |
| V2   | 0°          |      | 6°    | 0°       |      | 6°    |

Marking



Ordering information

| Order code | Package | Baseqty | Deliverymode  |
|------------|---------|---------|---------------|
| FQD17P06TM | TO-252  | 2500    | Tape and reel |



## Disclaimer

EVVOSEMI ("EVVO") reserves the right to make corrections, enhancements, improvements, and other changes to its products and services at any time, and to discontinue any product or service without notice.

EVVO warrants the performance of its hardware products to the specifications applicable at the time of sale in accordance with its standard warranty. Testing and other quality control techniques are used as deemed necessary by EVVO to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

Customers should obtain and confirm the latest product information and specifications before final design, purchase, or use. EVVO makes no warranty, representation, or guarantee regarding the suitability of its products for any particular purpose, nor does EVVO assume any liability for application assistance or customer product design. EVVO does not warrant or accept any liability for products that are purchased or used for any unintended or unauthorized application.

EVVO products are not authorized for use as critical components in life support devices or systems without the express written approval of EVVOSEMI.

The EVVO logo and EVVOSEMI are trademarks of EVVOSEMI or its subsidiaries in relevant jurisdictions. EVVO reserves the right to make changes without further notice to any products herein.