















ESD

TVS

MOS

LDO

Diode

Sensor

DC-DC

# **Product Specification**

Domestic Part Number	IRLR7843
Overseas Part Number	IRLR7843
▶ Equivalent Part Number	IRLR7843





96A

# N-Ch 30V Fast Switching MOSFETs

- ★ Super Low Gate Charge
- ★ 100% EAS Guaranteed
- ★ Green Device Available
- ★ Excellent CdV/dt effect decline
- ★ Advanced high cell density Trench technology

BVDSS	RDSON	ID

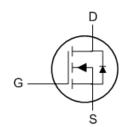
 $4m\Omega$ 

## **TO252 Pin Configuration**

**Product Summary** 

30V





#### **Description**

The IRLR7843 is the high cell density trenched N-ch MOSFETs, which provide excellent RDSON and gate charge for most of the synchronous buck converter applications. The IRLR7843 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### **Absolute Maximum Ratings**

		Ra	Rating		
Symbol	Parameter	10s	Steady State	Units	
V <sub>DS</sub>	Drain-Source Voltage	30		V	
$V_{GS}$	Gate-Source Voltage	=	±20		
I <sub>D</sub> @T <sub>C</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>		96		
I <sub>D</sub> @T <sub>C</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	68		Α	
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	30	19	Α	
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	25	16	Α	
I <sub>DM</sub>	Pulsed Drain Current <sup>2</sup>	192		Α	
EAS	Single Pulse Avalanche Energy <sup>3</sup> 144.7		mJ		
I <sub>AS</sub>	Avalanche Current 53.8		53.8	Α	
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	62.5		W	
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>4</sup>	6	2.42	W	
T <sub>STG</sub>	Storage Temperature Range	-55 to 175		°C	
TJ	Operating Junction Temperature Range	-55 to 175		°C	

#### **Thermal Data**

Symbol	Parameter	Тур.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>		62	°C/W
$R_{ heta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup> (t ≤10s)		25	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>		2.4	°C/W



# N-Ch 30V Fast Switching MOSFETs

#### Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
$\triangle BV_{DSS}/\triangle T_{J}$	BVDSS Temperature Coefficient	Reference to 25°C , I <sub>D</sub> =1mA		0.0213		V/°C
В	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =30A		3.4	4	0
R <sub>DS(ON)</sub>		V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		5.2	6	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V V I 250A	1.0	1.5	2.5	V
$\triangle V_{GS(th)}$	V <sub>GS(th)</sub> Temperature Coefficient	$V_{GS}=V_{DS}$ , $I_D=250uA$		-5.73		mV/°C
	Drain Course Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	uA
I <sub>DSS</sub>	Drain-Source Leakage Current	$V_{DS}$ =24V , $V_{GS}$ =0V , $T_J$ =55 $^{\circ}$ C			5	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =30A		26.5		S
$R_g$	Gate Resistance	V <sub>DS</sub> =0V , V <sub>GS</sub> =0V , f=1MHz		1.4	2.8	Ω
$Q_g$	Total Gate Charge (4.5V)	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		31.6		
$Q_gs$	Gate-Source Charge			8.6		nC
$Q_gd$	Gate-Drain Charge			11.7		
$T_{d(on)}$	Turn-On Delay Time			9		
Tr	Rise Time	$V_{DD}$ =15V , $V_{GS}$ =10V , $R_{G}$ =3.3 $\Omega$		19		
T <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> =15A		58		ns
T <sub>f</sub>	Fall Time			15.2		
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V , f=1MHz		3075	4000	
Coss	Output Capacitance			400	530	pF
C <sub>rss</sub>	Reverse Transfer Capacitance			315		

#### **Diode Characteristics**

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
I <sub>S</sub>	Continuous Source Current <sup>1,5</sup>	-V <sub>G</sub> =V <sub>D</sub> =0V , Force Current			96	Α
I <sub>SM</sub>	Pulsed Source Current <sup>2,5</sup>				192	Α
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C			1	V
t <sub>rr</sub>	Reverse Recovery Time	IF=30A , dI/dt=100A/μs ,		18		nS
Q <sub>rr</sub>	Reverse Recovery Charge	T <sub>J</sub> =25°C		8		nC

#### Note:

- 1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\,\leq\,300\text{us}$  , duty cycle  $\,\leq\,2\%$
- 3. The EAS data shows Max. rating . The test condition is  $V_{DD}$ =25V,  $V_{GS}$ =10V, L=0.1mH,  $I_{AS}$ =53.8A
- 4.The power dissipation is limited by 175°C junction temperature
- 5. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications , should be limited by total power dissipation.



## N-Ch 30V Fast Switching MOSFETs

#### **Typical Characteristics**

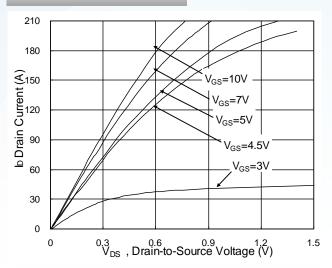


Fig.1 Typical Output Characteristics

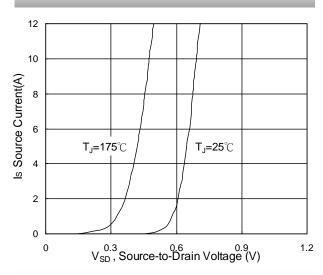


Fig.3 Forward Characteristics of Reverse

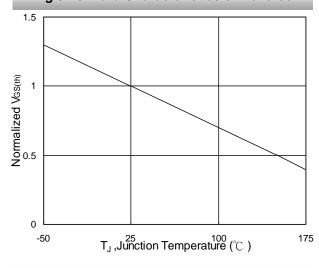


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

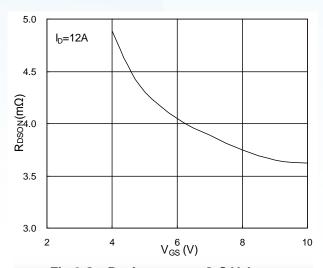


Fig.2 On-Resistance vs. G-S Voltage

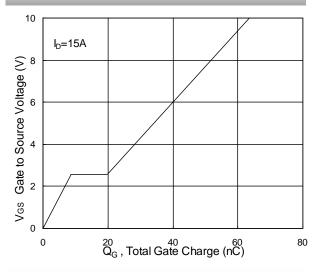


Fig.4 Gate-Charge Characteristics

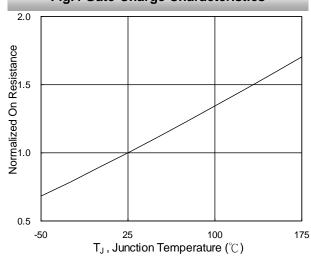
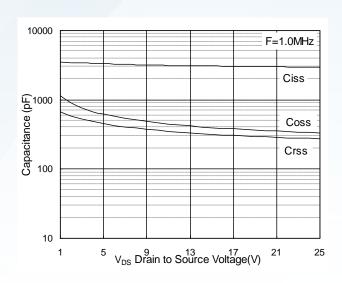


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>



# N-Ch 30V Fast Switching MOSFETs



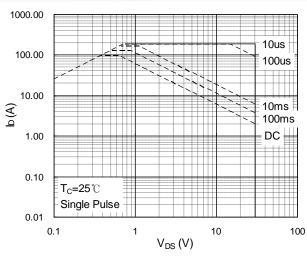


Fig.7 Capacitance

Fig.8 Safe Operating Area

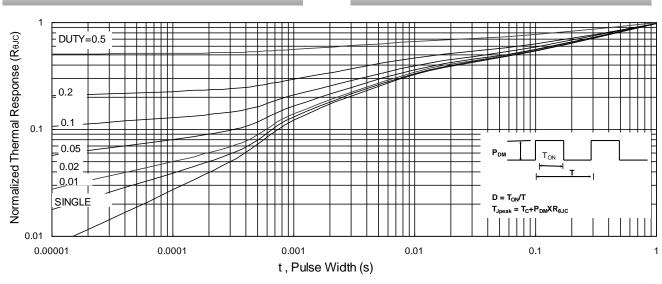


Fig.9 Normalized Maximum Transient Thermal Impedance

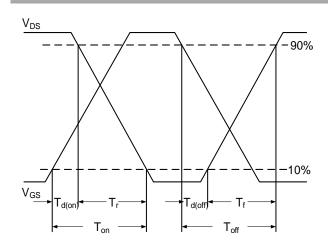


Fig.10 Switching Time Waveform

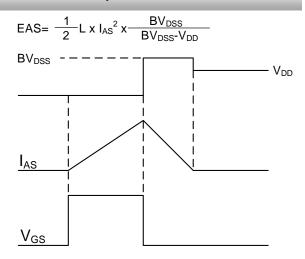


Fig.11 Unclamped Inductive Switching Waveform



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