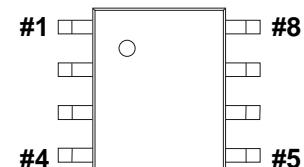


■ P-Channel MOSFET

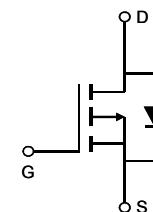
■ Features

- $V_{DS} (V) = -30V$
- $I_D = -12 A (V_{GS} = -20V)$
- $R_{DS(ON)} < 13m\Omega (V_{GS} = -20V)$
- $R_{DS(ON)} < 14m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 30m\Omega (V_{GS} = -5V)$



1.Source 5.Drain
2.Source 6.Drain
3.Source 7.Drain
4.Gate 8.Drain

■ Simplified outline(SOP-8)



■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	± 25	
Continuous Drain Current	TA=25°C	I_D	-12	A
	TA=70°C		-10	
Pulsed Drain Current		I_{DM}	-60	
Avalanche Current		I_{AS}, I_{AR}	26	
Power Dissipation	TA=25°C	P_D	3.1	W
	TA=70°C		2	
Avalanche energy	$L=0.3mH$	E_{AS}, E_{AR}	101	mJ
Thermal Resistance.Junction- to-Ambient	$t \leq 10s$	R_{thJA}	40	°C/W
	Steady-State		75	
Thermal Resistance.Junction- to-Case	Steady-State	R_{thJC}	24	
Junction Temperature		T_J	150	°C
Junction Storage Temperature Range		T_{stg}	-55 to 150	

■ Electrical Characteristics $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	$I_D=-250 \mu\text{A}, V_{GS}=0\text{V}$	-30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS}=-30\text{V}, V_{GS}=0\text{V}$			-1	μA
		$V_{DS}=-30\text{V}, V_{GS}=0\text{V}, T_J=55^\circ\text{C}$			-5	
Gate-Body leakage current	I_{GSS}	$V_{DS}=0\text{V}, V_{GS}=\pm 25\text{V}$			± 100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250 \mu\text{A}$	-1.7		-2.8	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-20\text{V}, I_D=-12\text{A}$			13	$\text{m}\Omega$
		$V_{GS}=-10\text{V}, I_D=-12\text{A}$			14	
		$V_{GS}=-10\text{V}, I_D=-12\text{A}, T_J=125^\circ\text{C}$			19	
		$V_{GS}=-5\text{V}, I_D=-7\text{A}$			30	
On state drain current	$I_{D(on)}$	$V_{GS}=-10\text{V}, V_{DS}=-5\text{V}$	-60			A
Forward Transconductance	g_{FS}	$V_{DS}=-5\text{V}, I_D=-10.5\text{A}$		27		S
Input Capacitance	C_{iss}	$V_{GS}=0\text{V}, V_{DS}=-15\text{V}, f=1\text{MHz}$		2060	2600	pF
Output Capacitance	C_{oss}			370		
Reverse Transfer Capacitance	C_{rss}			295		
Gate resistance	R_g	$V_{GS}=0\text{V}, V_{DS}=0\text{V}, f=1\text{MHz}$	1.2	2.4	3.6	Ω
Total Gate Charge	Q_g	$V_{GS}=-10\text{V}, V_{DS}=-15\text{V}, I_D=-12\text{A}$	24	30	36	nC
Gate Source Charge	Q_{gs}			4.6		
Gate Drain Charge	Q_{gd}			10		
Turn-On Delay Time	$t_{d(on)}$	$V_{GS}=-10\text{V}, V_{DS}=-15\text{V}, R_L=1.25\Omega, R_G=3\Omega$		11		ns
Turn-On Rise Time	t_r			9.4		
Turn-Off Delay Time	$t_{d(off)}$			24		
Turn-Off Fall Time	t_f			12		
Body Diode Reverse Recovery Time	t_{rr}	$I_F=-12\text{A}, dI/dt=100\text{A}/\mu\text{s}$		30	40	nC
Body Diode Reverse Recovery Charge	Q_{rr}			22		
Maximum Body-Diode Continuous Current	I_S				-4	A
Diode Forward Voltage	V_{SD}	$I_S=-1\text{A}, V_{GS}=0\text{V}$			-1	V

■ Typical Characteristics

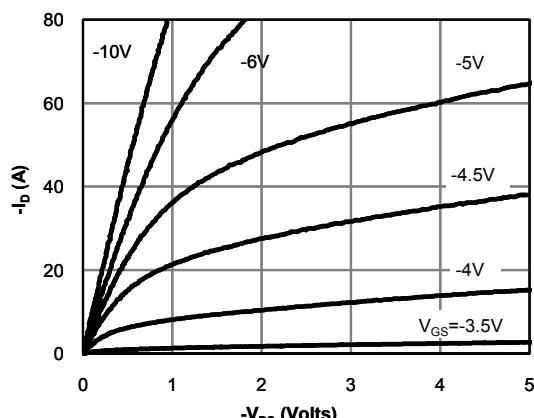


Fig 1: On-Region Characteristics (Note E)

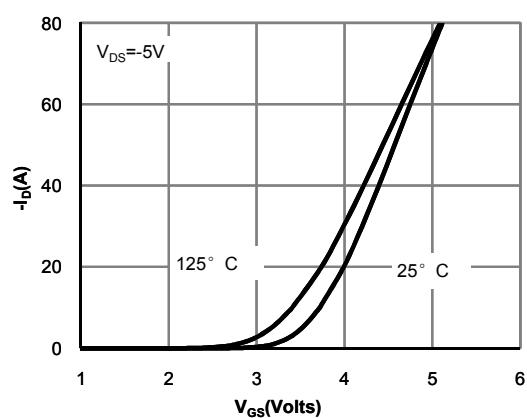


Figure 2: Transfer Characteristics (Note E)

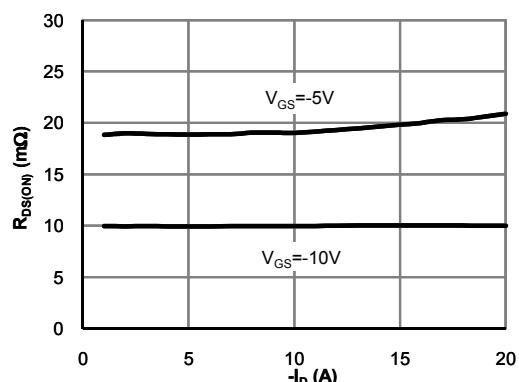


Figure 3: On-Resistance vs. Drain Current and Gate Voltage (Note E)

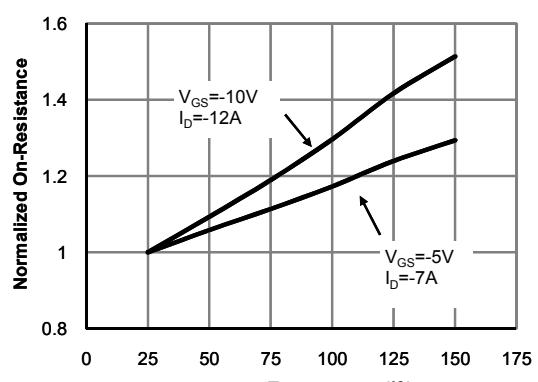


Figure 4: On-Resistance vs. Junction Temperature (Note E)

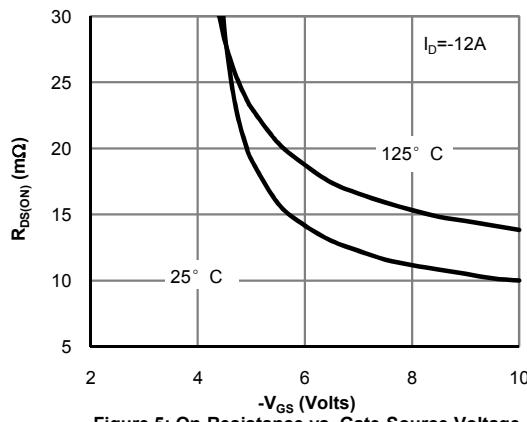


Figure 5: On-Resistance vs. Gate-Source Voltage (Note E)

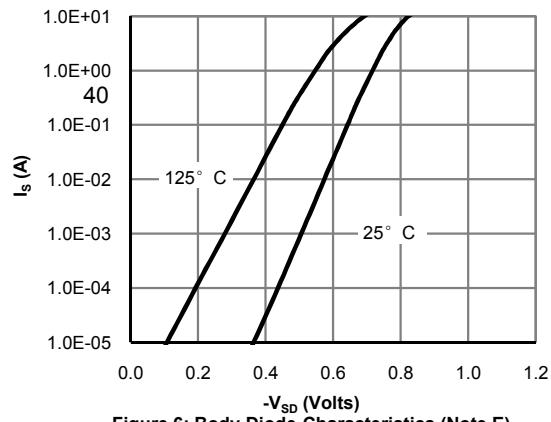
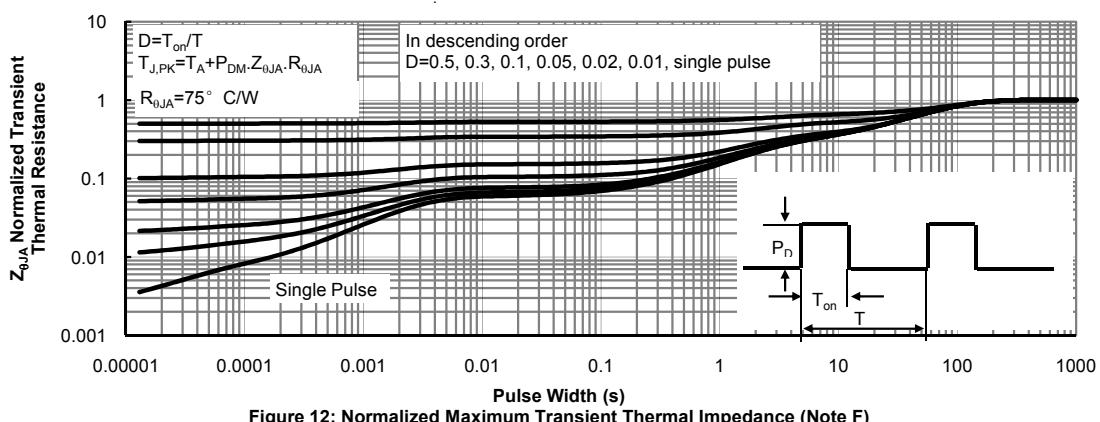
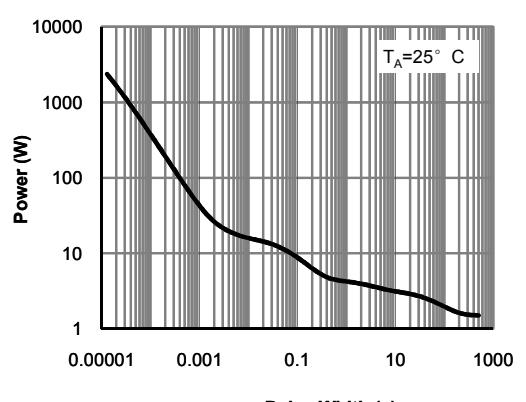
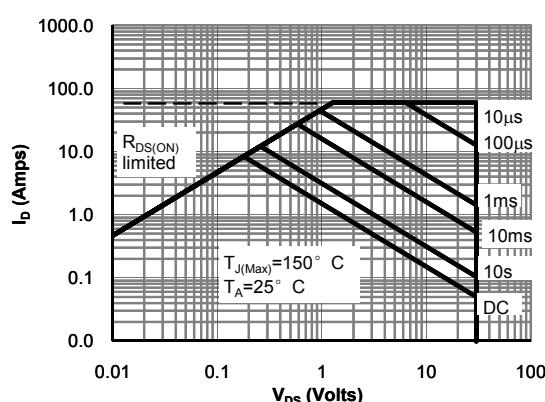
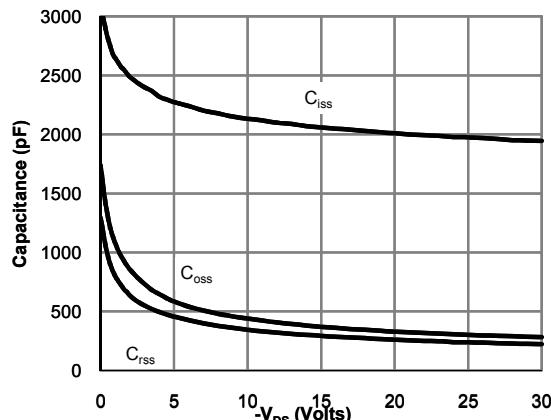
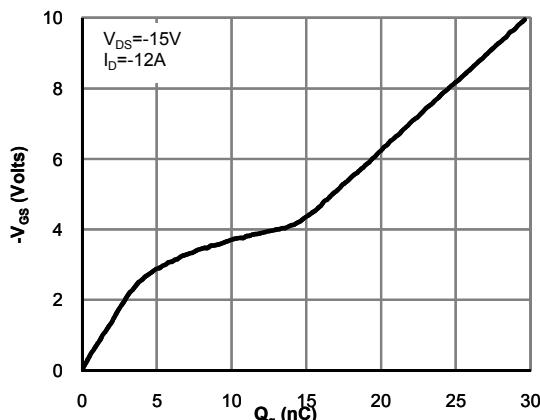
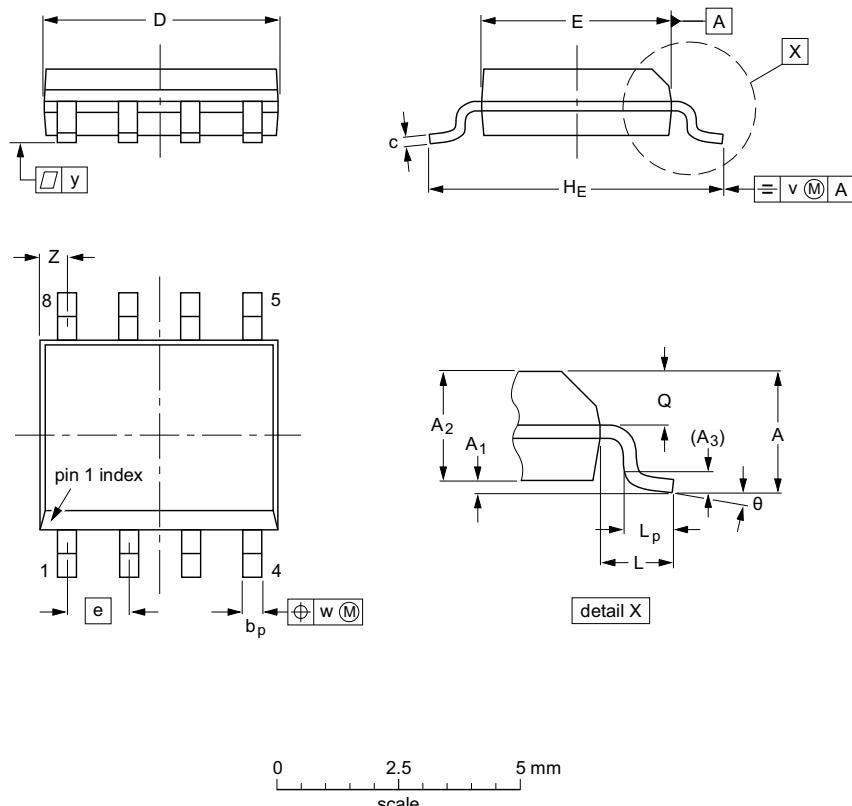


Figure 6: Body-Diode Characteristics (Note E)

■ Typical Characteristics



■ SOP-8



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A ₁	A ₂	A ₃	b _p	c	D ⁽¹⁾	E ⁽²⁾	e	H _E	L	L _p	Q	v	w	y	Z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	5.0 4.8	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8° 0°
inches	0.069	0.010 0.004	0.057 0.049	0.01	0.019 0.014	0.0100 0.0075	0.20 0.19	0.16 0.15	0.05	0.244 0.228	0.041	0.039 0.016	0.028 0.024	0.01	0.01	0.004	0.028 0.012	